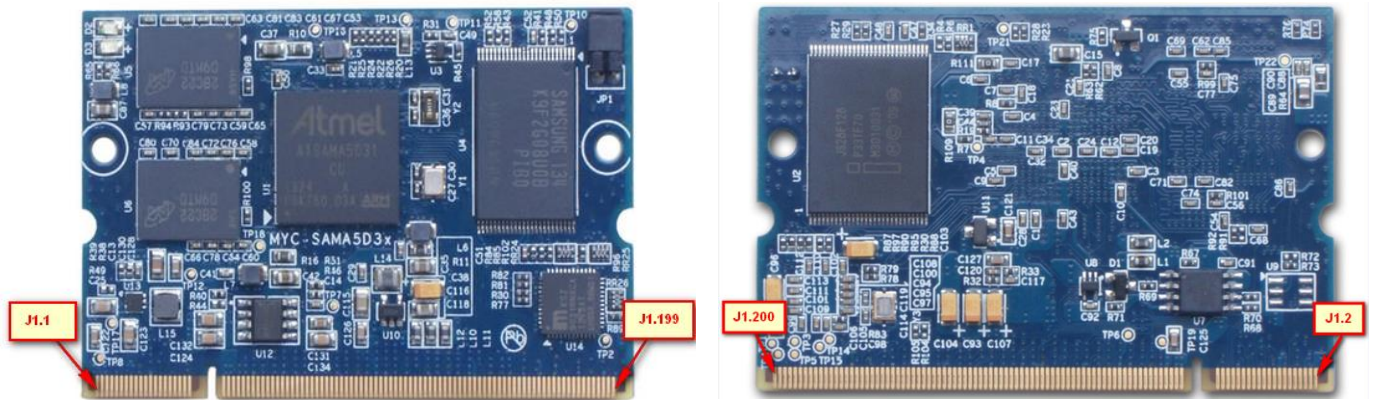


## Signals Routed to SO-DIMM 200-pin Connector

MYIR has designed a base board for the MYC-SAMA5D3X CPU Modules. The CPU Module is equipped with one SO-DIMM 200-pin connector which provides an interface to bring out all the I/O signals to or from the CPU Module and extended through the base board. Please refer to below table to know signals routed to the pins:



Function	IO Pins	Pin	Pin	IO Pins	Function
Blank	+5V (NC)	1	2	+5V (NC)	Blank
Blank	+5V (NC)	3	4	+5V (NC)	Blank
Power ground	<b>GND</b>	5	6	VDDBU	RTC Battery-powered
CTS2	PE23	7	8	PE29	GPIO
RTS2	PE24	9	10	PE30	Interrupt signal
RXD2	PE25	11	12	PE31	GPIO
TXD2	PE26	13	14	<b>GND</b>	Power ground
3V3 power	VDDIOM	15	16	VDDIOM	3V3 power
GPIO	PC25	17	18	PC24	GPIO
GPIO	PC23	19	20	PC22	GPIO
RD0	PC21	21	22	PC20	RF0
Power ground	<b>GND</b>	23	24	PC19	RK0
TD0	PC18	25	26	PC17	TF0
TK0	PC16	27	28	PC9	EMDIO
EMDC	PC8	29	30	PC7	EREFCK
ERXER	PC6	31	32	<b>GND</b>	Power ground
ETXEN	PC4	33	34	PC5	ECRSDV
ERX0	PC2	35	36	PC3	ERX1
ETX0	PC0	37	38	PC1	ETX1
PWR_EN	PWR_EN	39	40	CS_BOOT_DISABLE	FLASH Chip select
3V3 power	+3V3	41	42	+3V3	3V3 power
3V3 power	+3V3	43	44	+3V3	3V3 power
Blank	NC1	45	46	NC2	Blank
Blank	NC3	47	48	ADVREF	ADC Reference voltage
LCDDAT22	PE27	49	50	PE28	LCDDAT23
LCDDAT20	PC10	51	52	PC11	LCDDAT19
Power ground	<b>GND</b>	53	54	PC13	LCDDAT17
Function	IO Pins	Pin	Pin	IO Pins	Function

LCDDAT18	PC12	55	56	PC15	LCDDAT21
LCDDAT16	PC14	57	58	PC26	TWD1
TWCK1	PC27	59	60	PC28	GPIO
Interrupt signal	PC29	61	62	GND	Power ground
GPIO	PC31	63	64	PC30	GPIO
3V3 power	VDDIOP0	65	66	VDDIOP0	3V3 power supply
LCDDAT0	PA0	67	68	PA1	LCDDAT1
LCDDAT2	PA2	69	70	PA3	LCDDAT3
Power ground	GND	71	72	PA4	LCDDAT4
LCDDAT5	PA5	73	74	PA6	LCDDAT6
LCDDAT7	PA7	75	76	PA8	LCDDAT8
LCDDAT9	PA9	77	78	PA10	LCDDAT10
LCDDAT11	PA11	79	80	GND	Power ground
LCDDAT12	PA12	81	82	PA13	LCDDAT13
LCDDAT14	PA14	83	84	PA15	LCDDAT15
GPIO	PA16	85	86	PA17	GPIO
GPIO	PA18	87	88	PA19	GPIO
Power ground	GND	89	90	PA20	GPIO
GPIO	PA21	91	92	PA22	GPIO
GPIO	PA23	93	94	PA24	LCDPWM
LCDDISP	PA25	95	96	PA26	LCDVSYNC
LCDHSYNC	PA27	97	98	GND	Power ground
LCDPCK	PA28	99	100	PA29	LCDDEN
TWD0	PA30	101	102	PA31	TWCK0
3V3 power	VDDANA	103	104	VDDANA	3V3 power
PCK0	PD30	105	106	PD31	GPIO
Power ground	GND	107	108	PD29	GPIO
GPIO	PD28	109	110	PD27	GPIO
GPIO	PD26	111	112	PD25	GPIO
GPIO	PD24	113	114	PD23	AD3
AD2	PD22	115	116	GND	Power ground
AD0	PD20	117	118	PD21	AD1
Interrupt signal	PD18	119	120	PD19	GPIO
Interrupt signal	PD16	121	122	PD17	Interrupt signal
CANRX0	PD14	123	124	PD15	CANTX0
Power ground	GND	125	126	PD13	SPI0_NPCS0
SPI0_SPCK	PD12	127	128	PD11	SPI0_MOSI
SPI0_MISO	PD10	129	130	PD9	MCI0_CK
MCI0_DA7	PD8	131	132	PD7	MCI0_DA6
MCI0_DA5	PD6	133	134	GND	Power ground
MCI0_DA4	PD5	135	136	PD4	MCI0_DA3
MCI0_DA2	PD3	137	138	PD2	MCI0_DA1
MCI0_DA0	PD1	139	140	PD0	MCI0_CDA
3V3 power	VDDIOP1	141	142	VDDIOP1	3V3 power
Power ground	GND	143	144	NC4	Blank
<b>Function</b>	<b>IO Pins</b>	<b>Pin</b>	<b>Pin</b>	<b>IO Pins</b>	<b>Function</b>

GPIO	PB10	145	146	PB12	GPIO
CANRX1	PB14	147	148	PB15	CANTX1
MCI1_CDA	PB19	149	150	PB20	MCI1_DA0
MCI1_DA1	PB21	151	152	PB22	MCI1_DA2
MCI1_DA3	PB23	153	154	GND	Power ground
MCI1_CK	PB24	155	156	PB25	GPIO
Power ground	GND	157	158	PB27	RTS1
USBA_DP	USBA_DP	159	160	PB29	TXD1
USBA_DM	USBA_DM	161	162	PB31	DTXD (Debug)
Power ground	GND	163	164	PB30	DRXD (Debug)
USBB_DP	USBB_DP	165	166	PB26	CTS1
USBB_DM	USBB_DM	167	168	PB28	RXD1
Power ground	GND	169	170	GND	Power ground
USBC_DP	USBC_DP	171	172	DIBP	DIBP
USBC_DM	USBC_DM	173	174	DIBN	DIBN
Power ground	GND	175	176	GND	Power ground
ETH0_TX1+	ETH0_TX1+	177	178	JTAGSEL	JTAGSEL
ETH0_TX1-	ETH0_TX1-	179	180	WAKE UP	WAKE UP
ETH0_RX1+	ETH0_RX1+	181	182	SHDN	SHDN
ETH0_RX1-	ETH0_RX1-	183	184	BMS	BMS
Power ground	GND	185	186	NRST	NRST
ETH0_TX2+	ETH0_TX2+	187	188	NTRST	NTRST
ETH0_TX2-	ETH0_TX2-	189	190	TDI	TDI
ETH0_RX2+	ETH0_RX2+	191	192	TCK	TCK
ETH0_RX2-	ETH0_RX2-	193	194	TMS	TMS
Power ground	GND	195	196	TDO	TDO
ETH0_LED2	ETH0_LED2	197	198	NC5	Blank
ETH0_LED1	ETH0_LED1	199	200	GND	Power ground

**Function Block Diagram**

