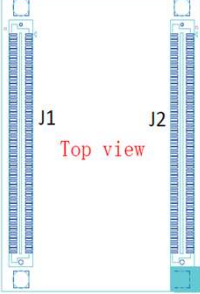


Main pin map table with columns: Function on Baseboard, SOC Pin Name, Bank, Ball, BTP Pin Name, J1, BTP Pin Name, Ball, Bank, SOC Pin Name, Function on Baseboard, Function on Baseboard, SOC Pin Name, Bank, Ball, BTP Pin Name, J2, BTP Pin Name, Ball, Bank, SOC Pin Name, Function on Baseboard.

NOTE:
\*\*MIO used for:
MIO[2:6] -- Quad SPI Flash Single SS 4012 IO
MIO7 -- USB PHY K61
MIO[8-9] -- CAN1
MIO[10:14] -- SD1 (Card detect EN0)
MIO[16:27] -- Enwt0
MIO[28:39] -- USB0
MIO[40:45] -- S08
MIO46 -- S08 d0
MIO47 -- S08 M0
MIO[48:49] -- UART1
MIO[50:51] -- I2C0
MIO[52:53] -- MIO10



BANK 112: only for Z-7815
BANK 13: only for Z-7820 and Z-7815