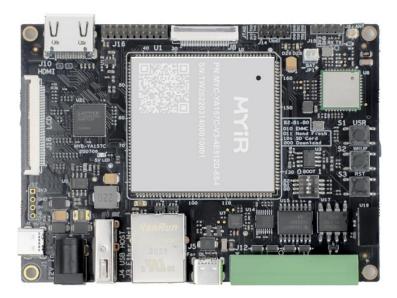




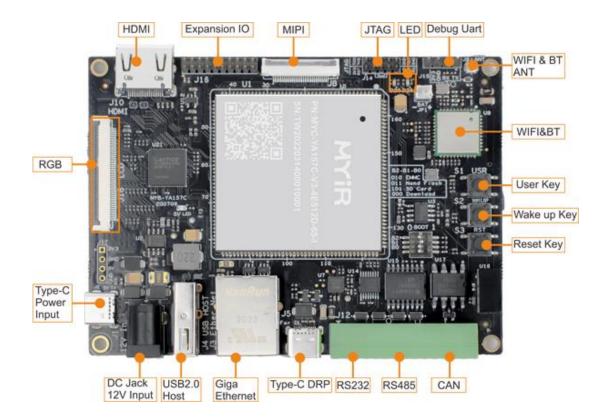
# MYD-YA157C Development Board

# **Overview**

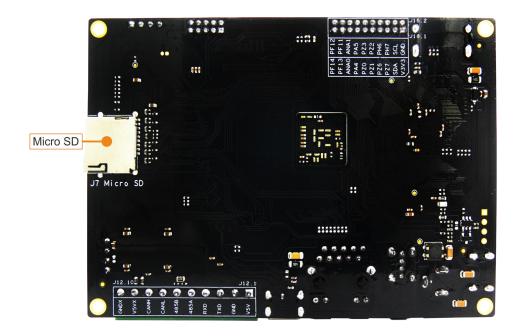


- ✓ MYC-YA157C CPU Module as Controller Board
- ✓ ST STM32MP1 MPU based on 650MHz Dual Arm Cortex-A7 and 209MHz Cortex-M4 Cores
- ✓ 512MB DDR3, 4GB eMMC Flash
- ✓ RS232, RS485, USB Type-C DRP, USB2.0 HOST, Gigabit Ethernet, CAN, WiFi/BT, Micro SD Card
- ✓ Supports RGB888 based LCD/HDMI and MIPI-DSI Display
- ✓ Supports Running Linux OS
- ✓ Optional 7-inch LCD Module and USB Camera Module

The <u>MYD-YA157C Development Board</u> consists of a compact CPU Module <u>MYC-YA157C</u> and a base board to provide a complete evaluation platform for <u>ST STM32MP1 Processors</u> which features dual-core Arm Cortex-A7 operating at up to 650 MHz and an embedded Cortex-M4 core operating at up to 209 MHz. Typical applications are industrial control, consumer electronics, smart home, medical and more other energy-efficient applications which require rich performance and low power.



MYD-YA157C Development Board (Top-view)



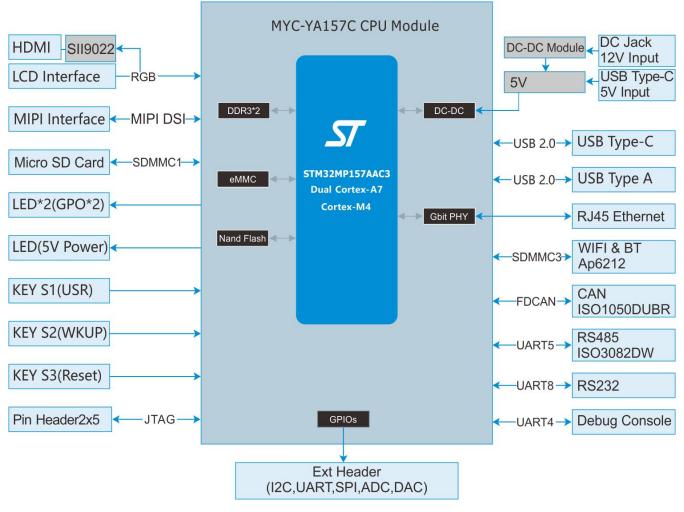
MYD-YA157C Development Board (Bottom-view)

# MYIR Make Your Idea Real

The MYC-YA157C CPU Module is populated on the base board through 1.0mm pitch 164-pin stamp-hole (Castellated-Hole) interface. It is a highly-integrated SoM which combines the STM32MP157 processor (STM32MP157AAC3), 512MB DDR3, 4GB eMMC as well as a GigE PHY chip. The base board has brought out rich peripherals through connectors and headers such as RS232, RS485, USB Type-C DRP, USB2.0 HOST, Gigabit Ethernet, WiFi/Bluetooth, CAN, Micro SD Card Slot, JTAG, RGB888 based LCD/HDMI, MIPI-DSI, etc.

The <u>MYD-YA157C Development Board</u> is delivered with one Quick Start Guide, one Type-C cable, one USB to TTL serial cable and one WiFi/Bluetooth antenna to provide user a complete platform for evaluating and prototyping based on STM32MP1 series microprocessors. MYIR also offers <u>MY-TFT070CV2 LCD Module</u> and <u>MY-CAM002U Camera Module</u> as add-on options for the board.

The MYD-YA157C is running Linux OS. Based on Linux 5.4.31 kernel, MYIR provides abundant software resources for Yocto 3.1 based MYIR MEasy-HMI system, ST Weston system and MYIR MEasy-IOT system as well as Ubuntu 18.04 system including kernel and driver source code, STM32CubeProgrammer and STM32CubeMX tools to enable users to start their development rapidly and easily.



MYD-YA157C Development Board

MYD-YA157C Development Board Function Block Diagram

#### **Hardware Specification**

The MYC-YA157C CPU Module is using STMicroelectronics <u>STM32MP157AAC3</u> Microprocessor with 12 x 12 mm, 0.5 mm pitch, TFBGA361 package which is among the <u>STM32MP1 Series</u>. The STM32MP1 series is based on a heterogeneous single or dual Arm Cortex-A7 and Cortex-M4 cores architecture, strengthening its ability to support multiple and flexible applications, achieving the best performance and power figures at any time. The Cortex-A7 core provides access to open-source operating systems (Linux/Android) while the Cortex-M4 core leverages the STM32 MCU ecosystem. It is available in 3 different lines which are pin-to-pin compatible:

- <u>STM32MP157</u>: Dual Cortex-A7 cores @ 650 MHz, Cortex-M4 core @ 209 MHz, 3D GPU, DSI display interface and CAN FD
- <u>STM32MP153</u>: Dual Cortex-A7 cores @ 650 MHz, Cortex-M4 core @ 209 MHz and CAN FD
- **<u>STM32MP151</u>**: Single Cortex-A7 core @ 650 MHz, Cortex-M4 core @ 209 MHz Each line comes with a security option (cryptography & secure boot)

2	ACCELERATION  Dual core Arm <sup>®</sup> Cortex <sup>®</sup> -A7 processor  L1 and L2 caches  3D Graphic Processing Unit*  Floating Point Unit + Arm <sup>®</sup> Neon <sup>TM</sup> Arm <sup>®</sup> Cortex <sup>®</sup> -M4 209 MHz	STM32 MP1 Product lines	Cortex <sup>e</sup> -A7 core	t <sub>oru</sub> (MHz)	Cortex <sup>®</sup> -M4 core	f <sub>acu</sub> (MHz)	3D GPU	f <sub>øru</sub> (MHz)	HW Crypto	FD-CAN	MIPI®-DSI
650 MHz	coprocessor         STI           MDMA + DMA         STI           LPDDR2/LPDDR3 16/32**-bit 533 MHz         STI           DDR3/DDR3L 16/32**-bit 533 MHz         STI           CONNECTIVITY         STI           2 x USB2.0 HS Host         STI           USB2.0 OTG FS/HS         STI           3 x SDMMC/SDI0         STI           2 x (TT)FD-CAN2.0*         STI           Gigabit Ethernet IEEE 1588***         FMC (NAND Rash)           Camera VF         Dual mode Quad-SPI	STM32MP151A	1	650	1	209	2	×.		2	21
A7 -		STM32MP151C							8.61		
Cortex®-		STM32MP153A	2	650	1	209	100	8		2	53
Arm®		STM32MP153C									
		STM32MP157A	2	650	1	209		533	•	2	
		STM32MP157C	2	000		209		999		2	

Notes:

\* Not available in all product lines

\*\* 16/32-bit for LFBGA448 and TFBGA361 packages, 16-bit only for LFBGA354 and TFBGA257 packages

\*\*\* 10/100M Ethernet only for LFBGA354 and TFBGA257 packages

#### STM32MP1 Series Processors

Arm <sup>®</sup> Dual Cortex <sup>®</sup> -A7 650 MHz L1 32kB 1 L1 32kB D 256kB L2 Cache FPU MPU							
External Memories	External Memories DDR3/DDR3L/LPDDR2/LPDDR3 32-bit @ 533 MHz						
3x SDMMC	Dual Quad-SPI	16-bit SLC NAND 8-bit ECC					
Internal Memories	MCU System RAM 384kB	MCU Retention RAM 64kB					
System RAM 256kB	Back up RAM 4kB	OTP fuse 3kb					
	Graphics	System					
Connectivity	3D GPU OpenGL ES 2.0 @ 533 MHz	5x LDOs Internal and External Oscillators					
10/100M or Gigabit	MIPI-DSI controller						
Ethernet GMAC	LCD-TFT controller	MDMA + 2x DMA					
3x USB 2.0 Host/0TG with 2x HS PHY		Reset and Clock					
Camera interface	Security	3x watchdogs					
HDMI-CEC	TrustZone	Up to 176 GPIOs					
2x CAN FD	AES 256, TDES*	Control					
MDIO slave	SHA-256, MD5, HMAC						
DFSDM (8 channels/6 filters)	3x Tamper Pins with 1 active	2x 16-bit advanced motor control timers 15x 16-bit timers 2x 32-bit timers					
6x SPI / 3x I <sup>2</sup> S	Secure Boot*						
6x I <sup>2</sup> C	Secure RAMs						
4x UART + 4x USART	Secure Peripherals	Analog					
4x SAI SPDIF	Secure RTC						
	Analog true RNG	2x 16-bit ADCs					
	96-bit unique ID	2x 12-bit DACs					

\*available for STM32MP157C only

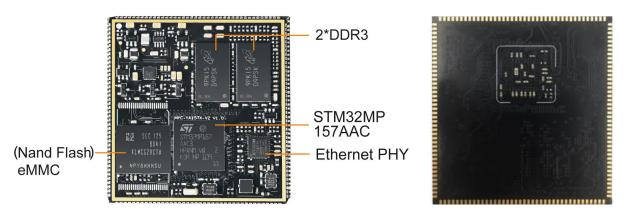
STM32MP157 Block Diagram

The MYD-YA157C Development Board is using MYC-YA157C CPU Module as core controller board. It takes full features of STM32MP1 processor and the main features are characterized as below:

# **Mechanical Parameters**

- Dimensions: 110mm x 80mm (base board), 45mm x 43mm (CPU Module)
- PCB Layers: 4-layer design (base board), 8-layer design (CPU Module)
- Power supply: +12V/1.5A or USB Type-C Power supply (base board), 5V/0.5A (CPU Module)
- Working temperature: 0~70 Celsius (commercial grade) or -40~85 Celsius (industrial grade)

## The MYD-YA157C Controller Board (MYC-YA157C CPU Module)



MYC-YA157C CPU Module without shielding cover (Top-view and Bottom-view)

#### Processor

- STMicroelectronics STM32MP157AAC3 Microprocessor
  - Up to 650MHz dual-core Arm Cortex-A7 32-bit RISC core
  - Up to 209MHz Arm Cortex-M4 32-bit RISC core with FPU/MPU
  - Integrated 3D GPU

## Memory

- 512MB DDR3 (supports up to 1GB DDR3)
- 4GB eMMC Flash (supports up to 64GB eMMC)
- Nand Flash (alternative design with eMMC, supporting 256MB / 512MB /1GB Nand Flash)

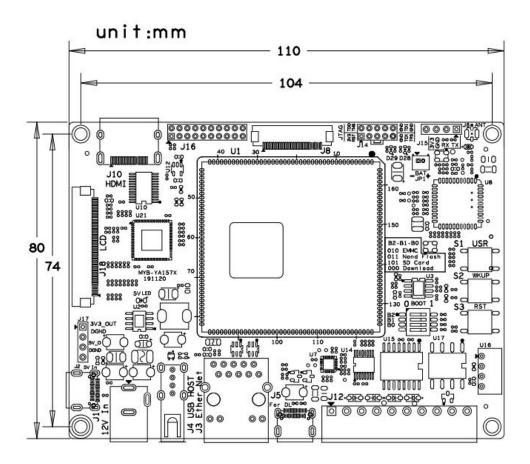
## Peripherals and Signals Routed to Pins

- One 10/100/1000M Ethernet PHY
- 1.0mm pitch 164-pin Stamp Hole Expansion Interface
  - 8 x Serial ports
  - 6 x I2C
  - 6 x SPI
  - 1 x SAI
  - 1 x USB 2.0 Host and 1 x USB 2.0 OTG
  - 2 x SDIO
  - 2 x CAN
  - 1 x MIPI-DSI
  - 1 x Digital Camera Interface (DCMI)
  - 1 x RGB Interface (supports RGB888, resolution up to 1366 x 768 @60fps)
  - Up to 97 GPIOs

Note: the peripheral signals brought out to the expansion interface are listed in maximum number. Some signals are reused. Please refer to the processor datasheet and the CPU Module pinout description file.

# The MYD-YA157C Development Board Base Board

- Serial ports
  - Debug UART
  - 1 x RS485, isolated power signal
  - 1 x RS232
- USB
  - 1 x USB2.0 Host port
  - 1 x USB Type-C DRP
- 1 x CAN, isolated power signal
- 1 x JTAG Interface (2.0mm pitch 2 x 5-pin headers)
- 1 x 10/100/1000 Mbps Ethernet interface (RJ45)
- WiFi/Bluetooth Module (complies with IEEE 802.11 b/g/n standard and supports Bluetooth V4.2)
- 1 x External antenna connector (simultaneous BT/WLAN receive with single antenna)
- 1 x Micro SD card slot
- RGB888 based LCD/HDMI (supports resolution up to 1366 x 768 pixels at 60Hz)
- 1 x MIPI-DSI Display Interface (supports display resolution up to 1366 x 768 pixels at 60Hz)
- 3 x Buttons (one for Wake up, one for Reset and one for USER)
- 1 x 2.0mm 2\*10-pin male expansion header



MYD-YA157C Base Board Dimensions Chart

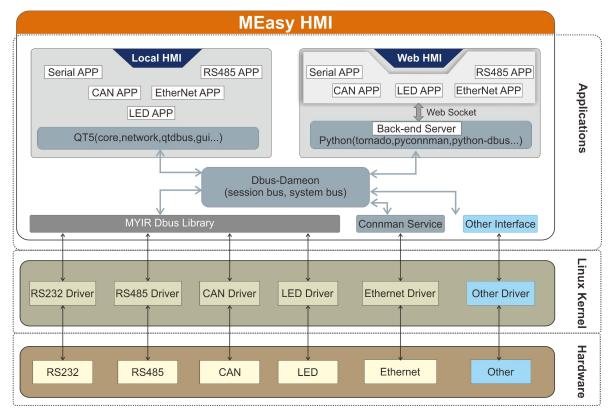


Item	Features	Description	Source Code	
Bootstrap program	TF-a-2.2	Arm Trusted Firmware	YES	
Bootloader	pader U-boot-2020.01 Kernel bootstrap		YES	
Linux kernel	Linux-5.4.31	Customized based on ST kernel_5.4.31 version for MYD-YA157C	YES	
	Nand Flash	Nand Flash driver	YES	
	PMIC	STPMIC driver		
	USB Host	USB Host driver	YES	
	USB OTG	USB OTG driver	YES	
	I2C	I2C driver	YES	
	SPI	SPI driver	YES	
	Ethernet	10M/100M/1000M Ethernet driver	YES	
	ММС	eMMC/TF card driver	YES	
	LCD	LCD driver, supports MYIR's 7-inch LCD with 800 x 480 pixels resolution	YES	
Drivers	HDMI	HDMI driver	YES	
	Touch	Capacitive touch screen driver	YES	
	PWM	PWM driver	YES	
	RTC	RTC driver	YES	
	GPIO	GPIO driver	YES	
	UART/USART	Serial port driver	YES	
	CAN	FDCAN Bus driver	YES	
	RS485	RS485 driver	YES	
	Camera	USB Camera driver (OV2659)	YES	
	WiFi & BT	AP6212 WiFi/BT driver (SDIO)	YES	
	Watchdog	Watchdog driver	YES	
	rootfs	Yocto 3.1 for ST Weston system	YES	
	rootfs	Yocto 3.1 for QT5.12 system	YES	
File system	rootfs	MEasy-IOT 1.0 & MEasy_HMI 2.0 demo system developed by MYIR	YES	
	Ubuntu core system	Based on ubuntu18.04	YES	
	STM32CubeProgrammer	ST programmer software	BIN	
Tools	STM32CubeMX	ST configuration integration tool	BIN	
	GPIO LED	LED example	YES	
	GPIO KEY	KEY example	YES	
	NET	TCP/IP Socket C/S example	YES	
	RTC	RTC example	YES	
	RS232	RS232 example	YES	
Applications	RS485	RS485 example	YES	
	CAN	CAN example	YES	
	LCD	LCD Display example	YES	
	Camera	Camera Display example	YES	
	UART	UART example	YES	
Compiler Tool Chain	Cross compiler	arm-openstlinux_weston-linux-gnueabi	BINARY	

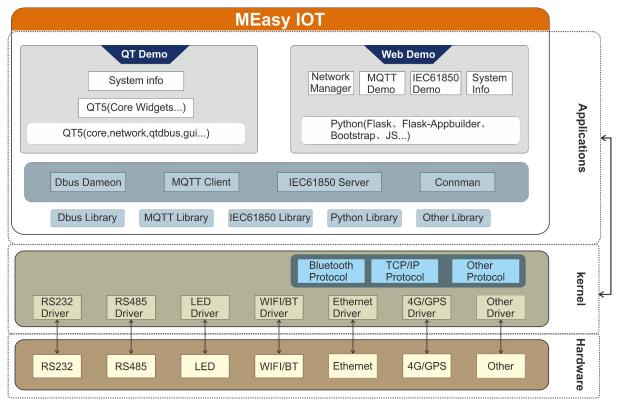
# MYD-YA157C Software Features

The MYD-YA157C runs Linux OS and is provided with software packages. Based on Linux 5.4.31 kernel, MYIR has provided abundant software resources for Yocto 3.1 based MYIR MEasy-HMI system, Yocto 3.1 based ST Weston system, Ubuntu 18.04 system and MYIR MEasy-IOT system including kernel and driver source code,

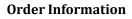
STM32CubeProgrammer and STM32CubeMX tools to enable users to start their development rapidly and easily.



MEasy-HMI System Structure



MEasy-IOT System Structure



Product Item	Part No.	Packing List
	MYD-YA157C-V3-4E512D-65-C	✓ One MYD-YA157C Development Board
MYD-YA157C		✓ One USB Type-C cable
Development Board		✓ One USB to UART Serial cable
Development board	MYD-YA157C-V3-4E512D-65-I	✓ One WiFi/Bluetooth Antenna
		✓ One Quick Start Guide
MYC-YA157C	MYC-YA157C-V3-4E512D-65-C	✓ One MYC-YA157C CPU Module
CPU Module	MYC-YA157C-V3-4E512D-65-I	
MY-LCD70TP-C	MV TETOZOCU2	✓ 7-inch LCD Module with capacitive touch screen
LCD Module	MY-TFT070CV2	
MY-CAM002U	MY CAMOODU	✓ USB Camera Module
Camera Module	MY-CAM002U	

Note:

1. One MYD-YA157C Development Board includes one CPU module MYC-YA157C mounted on the base board. If you need more CPU module, you can order extra ones.

2. Discounts are available for bulk orders.

3. We provide OEM/ODM services to reduce time and save cost for customers.

4. The Part No. with the suffix "-I" indicates the products of industrial version, supporting working temperature -40 to 85 degree Celsius; the Part No. with the suffix "-C" indicates the products of commercial version, supporting working temperature 0 to 70 degree Celsius.



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