



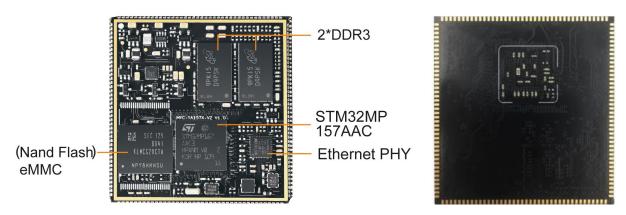
# MYC-YA157C CPU Module

## Overview



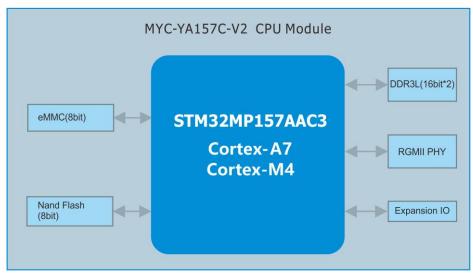
- ✓ STMicroelectronics STM32MP1 MPU based on 650MHz Dual Arm Cortex-A7 and 209MHz Cortex-M4 Cores
- ✓ 512MB DDR3, 4GB eMMC Flash
- ✓ On-board Gigabit Ethernet PHY
- ✓ 1.0mm pitch 164-pin Stamp Hole Expansion Interface
- Supports Running Linux

Measuring only 45mm by 43mm, the <u>MYC-YA157 CPU Module</u> is a compact <u>ST STM32MP1</u> powered System-on Module (SoM) that combines the <u>STM32MP157</u> processor (<u>STM32MP157AAC3</u>), 512MB DDR3, 4GB eMMC as well as an integrated GigE PHY chip. A number of peripherals and IO signals are brought out through 1.0 mm pitch 164-pin stamp-hole (Castellated-Hole) expansion interface to make the module an excellent embedded controller for your system integration. Typical applications are industrial control, consumer electronics, smart home, medical and more other energy-efficient applications which require rich performance and low power.



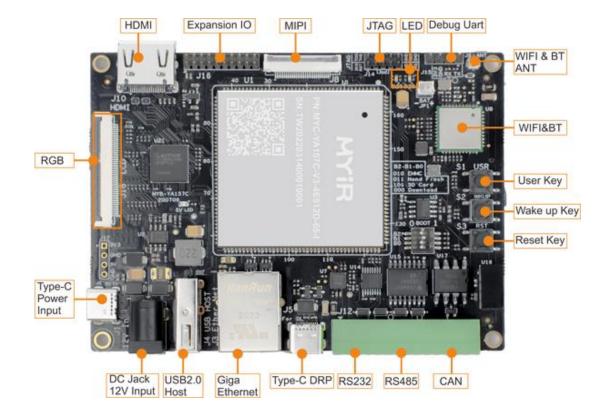
MYC-YA157C CPU Module without shielding cover (Top-view and Bottom-view)

The MYC-YA157C is running Linux OS. Based on Linux 5.4.31 kernel, MYIR provides abundant software resources for Yocto 3.1 based MYIR MEasy-HMI system, ST Weston system and MYIR MEasy-IOT system as well as Ubuntu 18.04 system including kernel and driver source code, STM32CubeProgrammer and STM32CubeMX tools to enable users to start their development rapidly and easily.

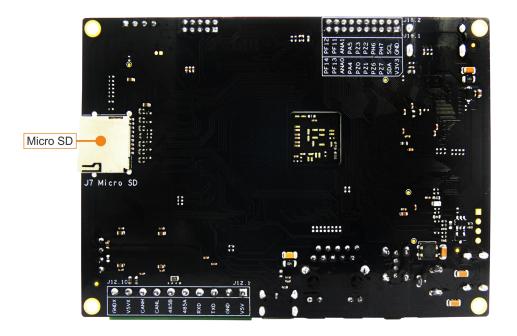


MYC-YA157C CPU Module Function Block Diagram

The <u>MYD-YA157C Development Board</u> is built around the <u>MYC-YA157C CPU Module</u>. It takes full advantages of the STM32MP157A MPU to explore a rich set of peripherals and interfaces to the base board including RS232, RS485, USB Type-C DRP, USB2.0 HOST, Gigabit Ethernet, WiFi/Bluetooth, CAN, Micro SD Card Slot, JTAG, RGB888 based LCD/HDMI, MIPI-DSI, etc. The <u>MYD-YA157C Development Board</u> is delivered with one Quick Start Guide, one Type-C cable, one USB to TTL serial cable and one WiFi/Bluetooth antenna to provide user a complete platform for evaluating and prototyping based on STM32MP1 series microprocessors. MYIR also offers <u>MY-TFT070CV2 LCD Module</u> and MY-CAM002U Camera Modules as add-on options for the board.



MYD-YA157C Development Board (Top-view)



MYD-YA157C Development Board (Bottom-view)

#### **Hardware Specification**

### MYIR Make Your Idea Real

The MYC-YA157C CPU Module is using STMicroelectronics <u>STM32MP157AAC3</u> Microprocessor with 12 x 12 mm, 0.5 mm pitch, TFBGA361 package which is among the <u>STM32MP1 Series</u>. The STM32MP1 series is based on a heterogeneous single or dual Arm Cortex-A7 and Cortex-M4 cores architecture, strengthening its ability to support multiple and flexible applications, achieving the best performance and power figures at any time. The Cortex-A7 core provides access to open-source operating systems (Linux/Android) while the Cortex-M4 core leverages the STM32 MCU ecosystem. It is available in 3 different lines which are pin-to-pin compatible:

- <u>STM32MP157</u>: Dual Cortex-A7 cores @ 650 MHz, Cortex-M4 core @ 209 MHz, 3D GPU, DSI display interface and CAN FD
- STM32MP153: Dual Cortex-A7 cores @ 650 MHz, Cortex-M4 core @ 209 MHz and CAN FD
- **<u>STM32MP151</u>**: Single Cortex-A7 core @ 650 MHz, Cortex-M4 core @ 209 MHz Each line comes with a security option (cryptography & secure boot)

	ACCELERATION Dual core Arm <sup>®</sup> Cortex <sup>®</sup> -A7 processor L1 and L2 caches 3D Graphic Processing Unit* Floating Point Unit + Arm <sup>®</sup> Neon™ Arm <sup>®</sup> Cortex <sup>®</sup> -M4 209 MHz	STM32 MP1 Product lines	Cortex <sup>e</sup> -A7 core	t <sub>oru</sub> (MHz)	Cortex <sup>e</sup> -M4 core	f <sub>acu</sub> (MHz)	30 GPU	f <sub>aru</sub> (MHz)	HW Crypto	FD-CAN	MIPI®-DSI
<sup>®</sup> -A7 - 650 MHz	coprocessor MDMA + DMA LPDDR2/LPDDR3 16/32**-bit 533 MHz DDR3/DDR3L 16/32**-bit 533 MHz CONNECTIVITY 2 x USB2.0 HS Host USB2.0 OTG FS/HS 3 x SDMMC/SDI0 USART, UART, SPI, I°C 2 x (TT)FD-CAN2.0*	STM32MP151A	1	650	1	209	1	4			1
		STM32MP151C									
Cortex®-A7		STM32MP153A	2	650	1	209	•	×		2	×
Arm		STM32MP153C									
	<ul> <li>Gigabit Ethernet IEEE 1588***</li> <li>FMC (NAND Rash)</li> <li>Camera VF</li> </ul>	STM32MP157A	2	650	1	209		533		2	
	Dual mode Quad-SPI DSI 2 Gbit/s*	STM32MP157C	5	000	đ.	200		000		-	

#### Notes:

\* Not available in all product lines

\*\* 16/32-bit for LFBGA448 and TFBGA361 packages, 16-bit only for LFBGA354 and TFBGA257 packages

\*\*\* 10/100M Ethernet only for LFBGA354 and TFBGA257 packages

#### STM32MP1 Series Processors

## MYIR Make Your Idea Real

Arm <sup>®</sup> Dual Cortex <sup>®</sup> -A7 650 MHz L1 32kB I L1 32kB D 256kB L2 Cache FPU MPU					
External Memories DDR3/DDR3L/LPDDR2/LPDDR3 32-bit @ 533 MHz					
3x SDMMC	Dual Quad-SPI	16-bit SLC NAND 8-bit ECC			
Internal Memories	MCU System RAM 384kB	MCU Retention RAM 64kB			
System RAM 256kB	Back up RAM 4kB	OTP fuse 3kb			
	Graphics	System			
Connectivity	3D GPU OpenGL ES 2.0 @ 533 MHz	5x LDOs Internal and External Oscillators MDMA + 2x DMA			
10/100M or Gigabit	MIPI-DSI controller				
Ethernet GMAC	LCD-TFT controller				
3x USB 2.0 Host/OTG		Reset and Clock			
with 2x HS PHY Camera interface	Security	3x watchdogs			
HDMI-CEC	TrustZone	Up to 176 GPIOs			
2x CAN FD	AES 256, TDES*	2000 14 00			
MDIO slave	SHA-256, MD5, HMAC	Control			
DFSDM	3x Tamper Pins with	2x 16-bit advanced			
(8 channels/6 filters)	1 active	motor control timers			
6x SPI / 3x I <sup>2</sup> S	Secure Boot*	15x 16-bit timers			
6x I <sup>2</sup> C	Secure RAMs	2x 32-bit timers			
4x UART + 4x USART	Secure Peripherals				
4x SAI SPDIF	Secure RTC	Analog			
əruir	Analog true RNG	2x 16-bit ADCs			
	96-bit unique ID	2x 12-bit DACs			

\*available for STM32MP157C only

STM32MP157 Block Diagram



- Dimensions: 45mm x 43mm
- PCB Layers: 8-layer design
- Power supply: +5V/0.5A
- Working temperature: 0~70 Celsius (commercial grade) or 40~85 Celsius (industrial grade)

#### Processor

- STMicroelectronics STM32MP157AAC3 Microprocessor
  - Up to 650MHz dual-core Arm Cortex-A7 32-bit RISC core
  - Up to 209MHz Arm Cortex-M4 32-bit RISC core with FPU/MPU
  - Integrated 3D GPU

#### Memory

- 512MB DDR3 (supports up to 1GB DDR3)
- 4GB eMMC Flash (supports up to 64GB eMMC)
- Nand Flash (alternative design with eMMC, supporting 256MB / 512MB /1GB Nand Flash)

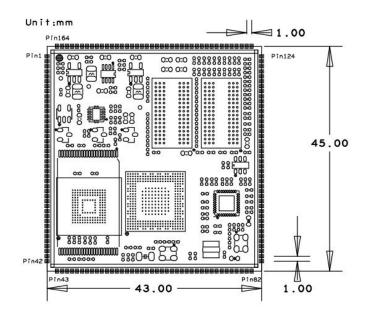
#### **Peripherals and Signals Routed to Pins**

- One 10/100/1000M Ethernet PHY
- 1.0mm pitch 164-pin Stamp Hole Expansion Interface
  - 8 x Serial ports
  - 6 x I2C
  - 6 x SPI
  - 1 x SAI
  - 1 x USB 2.0 Host and 1 x USB 2.0 OTG
  - 2 x SDIO
  - 2 x CAN
  - 1 x MIPI-DSI
  - 1 x Digital Camera Interface (DCMI)

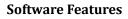
#### - 1 x RGB Interface (supports RGB888, resolution up to 1366 x 768 @60fps)

- Up to 97 GPIOs

Note: the peripheral signals brought out to the expansion interface are listed in maximum number. Some signals are reused. Please refer to the processor datasheet and the CPU Module pinout description file.



MYC-YA157C Dimensions Chart

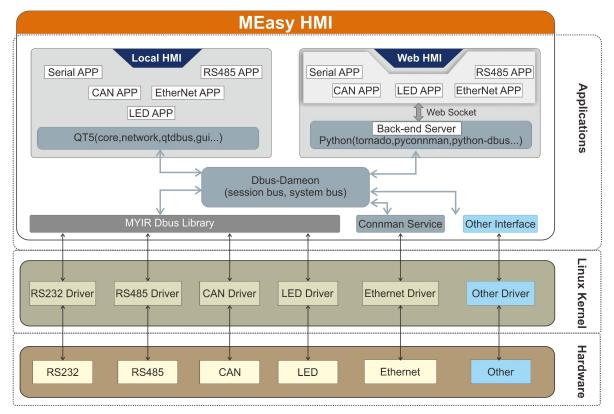


Item	Features	Description	Source Code
Bootstrap program	TF-a-2.2	Arm Trusted Firmware	YES
Bootloader	U-boot-2020.01	Kernel bootstrap	YES
Linux kernel	Linux-5.4.31	Customized based on ST kernel_5.4.31 version for MYD-YA157C	YES
	Nand Flash	Nand Flash driver	YES
	PMIC	STPMIC driver	YES
	USB Host	USB Host driver	YES
	USB OTG	USB OTG driver	
	I2C	I2C driver	YES
	SPI	SPI driver	YES
	Ethernet	10M/100M/1000M Ethernet driver	YES
	ММС	eMMC/TF card driver	YES
	LCD	LCD driver, supports MYIR's 7-inch LCD with 800 x 480 pixels resolution	YES
Drivers	HDMI	HDMI driver	YES
	Touch	Capacitive touch screen driver	YES
	PWM	PWM driver	YES
	RTC	RTC driver	YES
	GPIO	GPIO driver	YES
	UART/USART	Serial port driver	YES
	CAN	FDCAN Bus driver	YES
	RS485	RS485 driver	YES
	Camera	USB Camera driver (OV2659)	YES
	WiFi & BT	AP6212 WiFi/BT driver (SDIO)	YES
	Watchdog	Watchdog driver	YES
	rootfs	Yocto 3.1 for ST Weston system	YES
	rootfs	Yocto 3.1 for QT5.12 system	YES
File system	rootfs	MEasy-IOT 1.0 & MEasy_HMI 2.0 demo system developed by MYIR	YES
	Ubuntu core system	Based on ubuntu18.04	YES
	STM32CubeProgrammer	ST programmer software	BIN
Tools	STM32CubeMX	ST configuration integration tool	BIN
	GPIO LED	LED example	YES
	GPIO KEY	KEY example	YES
	NET	TCP/IP Socket C/S example	YES
	RTC	RTC example	YES
A 11	RS232	RS232 example	YES
Applications	RS485	RS485 example	YES
	CAN	CAN example	YES
	LCD	LCD Display example	YES
	Camera	Camera Display example	YES
	UART	UART example	YES
	Cross compiler	arm-openstlinux_weston-linux-gnueabi	BINARY

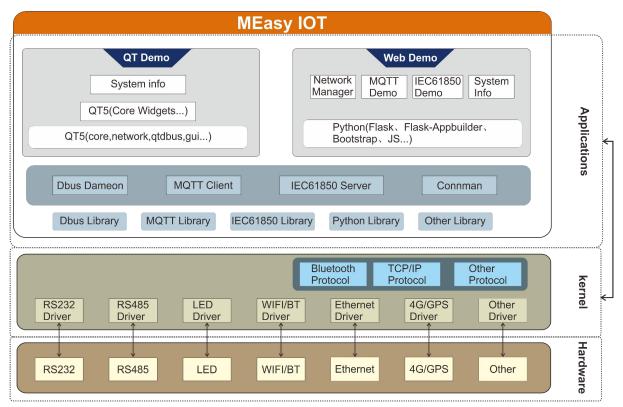
MYD-YA157C Software Features

The MYD-YA157C runs Linux OS and is provided with software packages. Based on Linux 5.4.31 kernel, MYIR has provided abundant software resources for Yocto 3.1 based MYIR MEasy-HMI system, Yocto 3.1 based ST Weston system, Ubuntu 18.04 system and MYIR MEasy-IOT system including kernel and driver source code,

STM32CubeProgrammer and STM32CubeMX tools to enable users to start their development rapidly and easily.



MEasy-HMI System Structure



MEasy-IOT System Structure

#### **Order Information**

Product Item	Part No.	Packing List
MYC-YA157C	MYC-YA157C-V3-4E512D-65-C	✓ One MYC-YA157C CPU Module
CPU Module	MYC-YA157C-V3-4E512D-65-I	
	MYD-YA157C-V3-4E512D-65-C	✓ One MYD-YA157C Development Board
MYD-YA157C		✓ One USB Type-C cable
	MYD-YA157C-V3-4E512D-65-I	✓ One USB to UART Serial cable
Development Board		✓ One WiFi/Bluetooth Antenna
		✓ One Quick Start Guide
MY-LCD70TP-C	MY-TFT070CV2	✓ 7-inch LCD Module with capacitive touch screen
LCD Module	MI-IFI0/0CV2	
MY-CAM002U		✓ USB Camera Module
Camera Module	MY-CAM002U	

Note:

1. One MYD-YA157C Development Board includes one CPU module MYC-YA157C mounted on the base board. If you need more CPU module, you can order extra ones.

2. Discounts are available for bulk orders.

3. We provide OEM/ODM services to reduce time and save cost for customers.

4. The Part No. with the suffix "-I" indicates the products of industrial version, supporting working temperature -40 to 85 degree Celsius; the Part No. with the suffix "-C" indicates the products of commercial version, supporting working temperature 0 to 70 degree Celsius.



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