

Num	Pin Name	BGA289 Ball	Pin Multiplexer			Reset state	Power Rail	Comment
			Signal Name	Mode	Type			
1	NC	-	-	-	-	-		
2	NC	-	-	-	-	-		
3	NC	-	-	-	-	-		
4	NC	-	-	-	-	-		
5	GND	-	-	-	Ground	-	Ground	
6	VBAT	-	-	-	Power	-	VDDBU	
7	NC	-	-	-	-	-		
8	NC	-	-	-	-	-		
9	NC	-	-	-	-	-		
10	NC	-	-	-	-	-		
11	NC	-	-	-	-	-		
12	PB1	A8	PB1	Primary	I/O	PIO, I, PU, ST	3.3V	
			A22/NANDCLE	B	O			
			SPIO_SPCK	C	I/O			
			PWML1	D	O			
			CLASSD_R0	F	O			
13	PB0	J8	PB0	Primary	I/O	PIO, I, PU, ST	3.3V	Has connected this pin to blue LED (D2) on CPU module.
			A21/NANDALE	B	A21/NANDALE			
			SPIO_MOSI	C	SPIO_MOSI			
			PWMH1	D	PWMH1			
14	GND	-	-	-	Ground	Ground		
15	NC	-	-	-	-	-		
16	NC	-	-	-	-	-		
17	PB2	A7	PB2	Primary	I/O	PIO, I, PU, ST	3.3V	
			NRD/NANDOE	B	O			
			PWMFIO	D	I			
			CLASSD_R1	F	O			
18	PB3	A6	PB3	Primary	I/O	PIO, I, PU, ST	3.3V	
			URXD4	A	I			
			D8	B	I/O			
			IRQ	C	I			
			PWMEXTRG1	D	I			
			CLASSD_R2	F	O			
19	PB4	B6	PB4	Primary	I/O	PIO, I, PU, ST	3.3V	
			UTXD4	A	O			
			D9	B	I/O			
			FIQ	C	I			
			CLASSD_R3	F	O			
20	PB5	B7	PB5	Primary	I/O	PIO, I, PU, ST	3.3V	
			TCLK2	A	I			
			D10	B	I/O			
			PWMH2	C	O			
			QSPI1_SCK	D	O			
			GTSUCOMP	F	O			
21	PB6	C7	PB6	Primary	I/O	PIO, I, PU, ST	3.3V	
			TIOA2	A	I/O			
			D11	B	I/O			
			PWML2	C	O			
			QSPI1_CS	D	O			
			GTXER	F	O			
22	PB7	C6	PB7	Primary	I/O	PIO, I, PU, ST	3.3V	
			TIOB2	A	I/O			
			D12	B	I/O			
			PWMH3	C	O			

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			Signal Name	Mode	Type			
			QSPI1_IO0	D	I/O			
			GRXCK	F	I			
23	GND	-			Ground		Ground	
24	PB9	A4	PB9	Primary	I/O	PIO, I, PU, ST	3.3V	
			TIOA3	A	I			
			D14	B	I/O			
			PWMFI1	C	I			
			QSPI1_IO2	D	I/O			
			GCOL	F	I			
25	PB8	A5	PB8	Primary	I/O	PIO, I, PU, ST	3.3V	
			TCLK3	A	I			
			D13	B	I/O			
			PWML3	C	O			
			QSPI1_IO1	D	I/O			
			GCRS	F	I			
26	PB11	B5	PB11	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT0	A	O			
			A0/NBS0	B	O			
			URXD3	C	I			
			PDMIC_DAT	D	I/O			
			GRX3	F	I			
27	PB10	H8	PB10	Primary	I/O	PIO, I, PU, ST	3.3V	
			TIOB3	A	I/O			
			D15	B	I/O			
			PWMEXTRG2	C	I			
			QSPI1_IO3	D	I/O			
			GRX2	F	I			
28	PB13	B4	PB13	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT2	A	O			
			A2	B	O			
			PCK1	C	O			
			GTX3	F	O			
29	PB12	D6	PB12	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT1	A	O			
			A1	B	O			
			UTXD3	C	O			
			PDMIC_CLK	D	O			
			GTX2	F	O			
30	PB25	B2	PB25	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT14	A	O			
			A14	B	O			
			RF0	C	I/O			
			FLEXCOM3_IO4	E	O			
			ISC_D11	F	I			
31	PB24	E5	PB24	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT13	A	O			
			A13	B	O			
			RK0	C	I/O			
			TCLK2	D	I			
			FLEXCOM3_IO3	E	O			
			ISC_D10	F	I			
32	GND	-			Ground		Ground	

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			Signal Name	Mode	Type			
33	PB26	E4	PB26	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT15	A	O			
			A15	B	O			
			URXD0	C	I			
			PDMIC_DAT	D	I/O			
			ISC_D0	F	I			
34	PB27	B1	PB27	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT16	A	O			
			A16	B	O			
			UTXD0	C	O			
			PDMIC_CLK	D	O			
			ISC_D1	F	I			
35	PB28	C2	PB28	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT17	A	O			
			A17	B	O			
			FLEXCOM0_IO0	C	I/O			
			TIOA5	D	I/O			
			ISC_D2	F	I			
36	PB29	D3	PB29	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT18	A	O			
			A18	B	O			
			FLEXCOM0_IO1	C	I/O			
			TIOB5	D	I/O			
			ISC_D3	F	I			
37	PB30	D2	PB30	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT19	A	O			
			A19	B	O			
			FLEXCOM0_IO2	C	I/O			
			TCLK5	D	I			
			ISC_D4	F	I			
38	PB31	C1	PB31	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT20	A	O			
			A20	B	O			
			FLEXCOM0_IO3	C	O			
			TWD0	D	I/O			
			ISC_D5	F	I			
39	NC	-						
40	BOOT_CS	-			I	I, PU	3.3V	When this pin become low,Disable all flash memory chip select on the CPU module.
41	VCC	-					3.3V	
42	VCC	-					3.3V	
43	VCC	-					3.3V	
44	VCC	-					3.3V	
45	PC0	P17	PC0	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT21	A	O			
			A23	B	O			
			FLEXCOM0_IO4	C	O			
			TWCK0	D	I/O			
			ISC_D6	F	I			

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			Signal Name	Mode	Type			
46	PC1	N12	PC1	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT22	A	O			
			A24	B	O			
			CANTX0	C	O			
			SPI1_SPCK	D	I/O			
			I2SCO_CK	E	I/O			
			ISC_D7	F	I			
47	PC2	N14	PC2	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT23	A	O			
			A25	B	O			
			CANRX0	C	I			
			SPI1_MOSI	D	I/O			
			I2SCO_MCK	E	O			
			ISC_D8	F	I			
48	ADDVREF	M6			POWER		3.3V	
49	PC4	M11	PC4	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDISP	A	O			
			NWR1/NBS1	B	O			
			TIOB1	C	I/O			
			SPI1_NPCS0	D	I/O			
			I2SCO_DIO	E	I			
			ISC_PCK	F	I			
50	PC3	M15	PC3	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDPWM	A	O			
			NWAIT	B	I			
			TIOA1	C	I/O			
			SPI1_MISO	D	I/O			
			I2SCO_WS	E	I/O			
			ISC_D9	F	I			
51	PC6	K10	PC6	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDHSYNC	A	O			
			NCS1	B	O			
			TWD1	C	I/O			
			SPI1_NPCS2	D	O			
			ISC_HSYNC	F	I			
52	PC5	L10	PC5	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDVSYNC	A	O			
			NCS0	B	O			
			TCLK1	C	I			
			SPI1_NPCS1	D	O			
			I2SCO_DO0	E	O			
			ISC_VSYNC	F	I			
53	GND	-			Ground		Ground	
54	PC7	M16	PC7	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDPCK	A	O			
			NCS2	B	O			
			TWCK1	C	I/O			
			SPI1_NPCS3	D	O			
			URXD1	E	I			
			ISC_MCK	F	O			
			PC8	Primary	I/O			
			LCDDEN	A	O			

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			Signal Name	Mode	Type			
55	PC8	J10	NANDRDY	B	I	PIO, I, PU, ST	3.3V	
			FIQ	C	I			
			PCK0	D	O			
			UTXD1	E	O			
			ISC_FIELD	F	I			
56	PC9	D1	PC9	Primary	I/O	PIO, I, PU, ST	3.3V	
			FIQ	A	I			
			GTSUCOMP	B	O			
			ISC_D0	C	I			
			TIOA4	D	I/O			
57	PC10	E3	PC10	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT2	A	O			
			GTXCK	B	I/O			
			ISC_D1	C	I			
			TIOB4	D	I/O			
			CANTX0	E	O			
58	PC11	E2	PC11	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT3	A	O			
			GTXEN	B	O			
			ISC_D2	C	I			
			TCLK4	D	I			
			CANRX0	E	I			
			A0/NBS0	F	O			
59	PC12	E1	PC12	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT4	A	O			
			GRXDV	B	I			
			ISC_D3	C	I			
			URXD3	D	I			
			TK0	E	I/O			
			A1	F	O			
60	PC13	F3	PC13	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT5	A	O			
			GRXER	B	I			
			ISC_D4	C	I			
			UTXD3	D	O			
			TF0	E	I/O			
			A2	F	O			
61	PC14	F5	PC14	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT6	A	O			
			GRX0	B	I			
			ISC_D5	C	I			
			TD0	E	O			
			A3	F	O			
62	GND	-			Ground	Ground		
63	PC16	G6	PC16	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT10	A	O			
			GTX0	B	O			
			ISC_D7	C	I			
			RK0	E	I/O			
			A5	F	O			
			PC15	Primary	I/O			
			LCDDAT7	A	O			

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			Signal Name	Mode	Type			
64	PC15	F2	GRX1	B	O	PIO, I, PU, ST	3.3V	
			ISC_D6	C	I			
			RD0	E	I			
			A4	F	O			
65	NC	-						
66	NC	-						
67	PC17	F1	PC17	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT11	A	O			
			GTX1	B	O			
			ISC_D8	C	I			
			RF0	E	I/O			
			A6	F	O			
68	PC18	H6	PC18	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT12	A	O			
			GMDC	B	O			
			ISC_D9	C	I			
			FLEXCOM3_IO2	E	I/O			
			A7	F	O			
69	PC19	G2	PC19	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT13	A	O			
			GMDIO	B	I/O			
			ISC_D10	C	I			
			FLEXCOM3_IO1	E	I/O			
			A8	F	O			
70	PC20	G3	PC20	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT14	A	O			
			GRXCK	B	I			
			ISC_D11	C	I			
			FLEXCOM3_IO0	E	I/O			
			A9	F	O			
71	GND	-			Ground	Ground		
72	PC22	H2	PC22	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT18	A	O			
			GCRS	B	I			
			ISC_VSYNC	C	I			
			FLEXCOM3_IO4	E	O			
			A11	F	O			
73	PC21	G1	PC21	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT15	A	O			
			GTXER	B	O			
			ISC_PCK	C	I			
			FLEXCOM3_IO3	E	O			
			A10	F	O			
74	PC24	H1	PC24	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT20	A	O			
			GRX2	B	I			
			ISC_MCK	C	O			
			A13	F	O			
75	PC23	G5	PC23	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT19	A	O			
			GCOL	B	I			
			ISC_HSYNC	C	I			

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			Signal Name	Mode	Type			
			A12	F	O			
76	PC26	J9	PC26	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT22	A	O			
			GTX2	B	O			
			CANTX1	D	O			
			A15	F	O			
77	PC25	H5	PC25	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT21	A	O			
			GRX3	B	I			
			ISC_FIELD	C	I			
			A14	F	O			
78	PC28	E8	PC28	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDPWM	A	O			
			FLEXCOM4_IO0	B	I/O			
			PCK2	C	O			
			TWCK0	E	I/O			
			A17	F	O			
79	PC27	H9	PC27	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDAT23	A	O			
			GTX3	B	O			
			PCK1	C	O			
			CANRX1	D	I			
			TWD0	E	I/O			
			A16	F	O			
80	GND	-			Ground	Ground		
81	PC29	G8	PC29	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDDISP	A	O			
			FLEXCOM4_IO1	B	I/O			
			A18	F	O			
82	PC30	F8	PC30	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDVSYNC	A	O			
			FLEXCOM4_IO2	B	I/O			
			A19	F	O			
83	PC31	D8	PC31	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDHSYNC	A	O			
			FLEXCOM4_IO3	B	O			
			URXD3	C	I			
			A20	F	O			
84	NC	-						
85	PD0	G10	PD0	Primary	I/O	PIO, I, PU, ST	3.3V	
			LCDPCK	A	O			
			FLEXCOM4_IO4	B	O			
			UTXD3	C	O			
			GTSUCOMP	D	O			
			A23	F	O			
86	NC	-						
87	PD2	E10	PD2	Primary	I/O	PIO, I, PU, ST	3.3V	
			URXD1	A	I			
			GTXER	D	O			
			ISC_MCK	E	O			
			A25	F	O			
			PD1	Primary	I/O			

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			Signal Name	Mode	Type			
88	PD1	G9	LCDDEN	A	O	PIO, I, PU, ST	3.3V	
			GRXCK	D	I			
			A24	F	O			
89	GND	-			Ground	Ground		
90	PD3	K1	PD3	Primary	I/O	PIO, I, PU, ST	3.3V	
			UTXD1	A	O			
			FIQ	B	I			
			GCRS	D	I			
			ISC_D11	E	I			
			NWAIT	F	I			
91	PD4	J6	PD4	Primary	I/O	PIO, I, PU, ST	3.3V	Has connected this pin to EEPROM(I2C) on CPU module, can not use it as GPIO on base board.
			TWD1	A	I/O			
			URXD2	B	I			
			GCOL	D	I			
			ISC_D10	E	I			
			NCS0	F	O			
92	PD5	J4	PD5	Primary	I/O	PIO, I, PU, ST	3.3V	Has connected this pin to EEPROM(I2C) on CPU module, can not use it as GPIO on base board.
			TWCK1	A	I/O			
			UTXD2	B	O			
			GRX2	D	I			
			ISC_D9	E	I			
			NCS1	F	O			
93	PD6	J2	PD6	Primary	I/O	PIO, I, PU, ST	3.3V	
			TCK	A	I			
			PCK1	B	O			
			GRX3	D	I			
			ISC_D8	E	I			
			NCS2	F	O			
94	PD7	J7	PD7	Primary	I/O	PIO, I, PU, ST	3.3V	
			TDI	A	I			
			UTMI_RXVAL	C	O			
			GTX2	D	O			
			ISC_D0	E	I			
			NWR1/NBS1	F	O			
95	PD8	J1	PD8	Primary	I/O	PIO, I, PU, ST	3.3V	
			TDO	A	O			
			UTMI_RXERR	C	O			
			GTX3	D	O			
			ISC_D1	E	I			
			NANDRDY	F	I			
96	PD9	K9	PD9	Primary	I/O	PIO, I, PU, ST	3.3V	
			TMS	A	I			
			UTMI_RXACT	C	O			
			GTXCK	D	I/O			
			ISC_D2	E	I			
97	PD10	J3	PD10	Primary	I/O	PIO, I, PU, ST	3.3V	
			NTRST	A	I			
			UTMI_HDIS	C	O			
			GTXEN	D	O			
			ISC_D3	E	I			
98	GND	-			Ground	Ground		
			PD12	Primary	I/O			



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			Signal Name	Mode	Type			
99	PD12	K8	TIOB1	A	I/O	PIO, I, PU, ST	3.3V	
			FLEXCOM4_IO0	B	I/O			
			UTMI_LS1	C	O			
			GRXER	D	I			
			ISC_D5	E	I			
			ISC_D4	F	I			
100	PD11	M1	PD11	Primary	I/O	PIO, I, PU, ST	3.3V	
			TIOA1	A	I/O			
			PCK2	B	O			
			UTMI_LS0	C	O			
			GRXDV	D	I			
			ISC_D4	E	I			
			ISC_MCK	F	O			
101	PD14	K4	PD14	Primary	I/O	A, PU, ST	3.3V	
			TCK	A	I			
			FLEXCOM4_IO2	B	I/O			
			UTMI_CDRCPSSEL1	C	I			
			GRX1	D	I			
			ISC_D7	E	I			
			ISC_D6	F	I			
102	PD13	L2	PD13	Primary	I/O	PIO, I, PU, ST	3.3V	
			TCLK1	A	I			
			FLEXCOM4_IO1	B	I/O			
			UTMI_CDRCPSSEL0	C	I			
			GRX0	D	I			
			ISC_D6	E	I			
			ISC_D5	F	I			
103	NC	-						
104	NC	-						
105	NC	-						
106	PD15	K7	PD15	Primary	I/O	PIO, I, PU, ST	3.3V	
			TDI	A	I			
			FLEXCOM4_IO3	B	O			
			UTMI_CDRCPDIVEN	C	I			
			GTX0	D	O			
			ISC_PCK	E	I			
			ISC_D7	F	I			
107	GND	-			Ground	Ground		
108	PD17	K2	PD17	Primary	I/O	A, PU, ST	3.3V	
			TMS	A	I			
			UTMI_CDRCPSSELDIV	C	O			
			GMDC	D	O			
			ISC_HSYNC	E	I			
			ISC_D9	F	I			
109	PD16	L1	PD16	Primary	I/O	PIO, I, PU, ST	3.3V	
			TDO	A	O			
			FLEXCOM4_IO4	B	O			
			UTMI_CDRBISTEN	C	I			
			GTX1	D	O			
			ISC_VSYNC	E	I			
			ISC_D8	F	I			
			PD19	Primary	I/O			

Num	Pin Name	BGA289 Ball	Pin Multiplexer			Reset state	Power Rail	Comment
			Signal Name	Mode	Type			
110	PD19	K6	AD0	Alternate	I	PIO, I, PU, ST	3.3V	
			PCK0	A	O			
			TWD1	B	I/O			
			URXD2	C	I			
			I2SC0_CK	E	I/O			
			ISC_D11	F	I			
111	PD18	J5	PD18	Primary	I/O	PIO, I, PU, ST	3.3V	
			NTRST	A	I			
			GMDIO	D	I/O			
			ISC_FIELD	E	I			
			ISC_D10	F	I			
112	PD21	N1	PD21	Primary	I/O	PIO, I, PU, ST	3.3V	
			AD2	Alternate	I			
			TIOB2	A	I/O			
			TWD0	B	I/O			
			FLEXCOM4_IO0	C	I/O			
			I2SC0_WS	E	I/O			
			ISC_VSYNC	F	I			
113	PD20	M2	PD20	Primary	I/O	PIO, I, PU, ST	3.3V	
			AD1	Alternate	I			
			TIOA2	A	I/O			
			TWCK1	B	I/O			
			UTXD2	C	O			
			I2SC0_MCK	E	O			
			ISC_PCK	F	I			
114	PD23	M3	PD23	Primary	I/O	PIO, I, PU, ST	3.3V	
			AD4	Alternate	I			
			URXD2	A	I			
			FLEXCOM4_IO2	C	I/O			
			I2SC0_DO0	E	O			
			ISC_FIELD	F	I			
115	PD22	L4	PD22	Primary	I/O	PIO, I, PU, ST	3.3V	
			AD3	Alternate	I			
			TCLK2	A	I			
			TWCK0	B	I/O			
			FLEXCOM4_IO1	C	I/O			
			I2SC0_DI0	E	I			
			ISC_HSYNC	F	I			
116	GND	-			Ground	Ground		
117	PD24	L7	PD24	Primary	I/O	PIO, I, PU, ST	3.3V	
			AD5	Alternate	I			
			UTXD2	A	O			
			FLEXCOM4_IO3	C	O			
118	PD25	L6	PD25	Primary	I/O	PIO, I, PU, ST	3.3V	
			AD6	Alternate	I			
			SPI1_SPCK	A	I/O			
			FLEXCOM4_IO4	C	O			
119	PD26	N2	PD26	Primary	I/O	PIO, I, PU, ST	3.3V	
			AD7	Alternate	I			
			SPI1_MOSI	A	I/O			
			FLEXCOM2_IO0	C	I/O			
			PD27	Primary	I/O			

Num	Pin Name	BGA289 Ball	Pin Multiplexer			Reset state	Power Rail	Comment
			Signal Name	Mode	Type			
120	PD27	L8	AD8	Alternate	I	PIO, I, PU, ST	3.3V	
			SPI1_MISO	A	I/O			
			TCK	B	I			
			FLEXCOM2_IO1	C	I/O			
121	PD28	M4	PD28	Primary	I/O	PIO, I, PU, ST	3.3V	
			AD9	Alternate	I			
			SPI1_NPCS0	A	I/O			
			TDI	B	I			
122	PD29	N3	FLEXCOM2_IO2	C	I/O	PIO, I, PU, ST	3.3V	
			PD29	Primary	I/O			
			AD10	Alternate	I			
			SPI1_NPCS1	A	O			
			TDO	B	O			
			TIOA3	D	I/O			
123	PD31	M7	TWD0	E	I/O	PIO, I, PU, ST	3.3V	
			PD31	Primary	I/O			
			ADTRG	A	I			
			NTRST	B	I			
			IRQ	C	I			
			TCLK3	D	I			
124	PD30	L9	PCK0	E	O	PIO, I, PU, ST	3.3V	
			PD30	Primary	I/O			
			AD11	Alternate	I			
			SPI1_NPCS2	A	O			
			TMS	B	I			
			FLEXCOM2_IO4	C	O			
125	GND	-			Ground	Ground		
126	NC	-						
127	PIOBU0	R3				VDDBU		
128	PIOBU5	P5				VDDBU		
129	PIOBU1	N8				VDDBU		
130	PIOBU6	P6				VDDBU		
131	PIOBU2	R2				VDDBU		
132	PIOBU7	M8				VDDBU		
133	PIOBU3	R5				VDDBU		
134	GND	-			Ground	Ground		
135	PIOBU4	R4				VDDBU		
136	NC	-						
137	NC	-						
138	NC	-						
139	NC	-						
140	NC	-						
141	NC	-						
142	NC	-						
143	GND	-			Ground	Ground		
144	PA15	N16	PA15	Primary	I/O	PIO, I, PU, ST	3.3V	Has connected this pin to SPI Flash on CPU module, can not use it as GPIO or on base board.
			SPIO_MOSI	A	I/O			
			TF1	B	I/O			
			QSPI0_CS	C	O			
			I2SC1_CK	D	I/O			
			FLEXCOM3_IO0	E	I/O			

Num	Pin Name	BGA289 Ball	Pin Multiplexer			Reset state	Power Rail	Comment
			Signal Name	Mode	Type			
			D10	F	I/O			
145	PA14	M14	PA14	Primary	I/O	PIO, I, PU, ST	3.3V	Has connected this pin to SPI Flash on CPU module,can not use it as GPIO or on base board.
			SPIO_SPCK	A	I/O			
			TK1	B	I/O			
			QSPIO_SCK	C	O			
			I2SC1_MCK	D	O			
			FLEXCOM3_IO2	E	I/O			
			D9	F				
146	PA17	N17	PA17	Primary	I/O	PIO, I, PU, ST	3.3V	Has connected this pin to SPI Flash on CPU module,can not use it as GPIO or on base board.
			SPIO_NPCS0	A	I/O			
			RD1	B	I			
			QSPIO_IO1	C	I/O			
			I2SC1_DIO	D	I			
			FLEXCOM3_IO4	E	O			
			D12	F	I/O			
147	PA16	M10	PA16	Primary	I/O	PIO, I, PU, ST	3.3V	Has connected this pin to SPI Flash on CPU module,can not use it as GPIO or on base board.
			SPIO_MISO	A	I/O			
			TD1	B	O			
			QSPIO_IO0	C	I/O			
			I2SC1_WS	D	I/O			
			FLEXCOM3_IO3	E	O			
			D11	F	I/O			
148	PA19	T14	PA19	Primary	I/O	PIO, I, PU, ST	3.3V	
			SPIO_NPCS2	A	O			
			RF1	B	I/O			
			QSPIO_IO3	C	I/O			
			TIOA0	D	I/O			
			SDMMC1_DAT1	E	I/O			
			D14	F	I/O			
149	PA18	U14	PA18	Primary	I/O	PIO, I, PU, ST	3.3V	
			SPIO_NPCS1	A	O			
			RK1	B	I/O			
			QSPIO_IO2	C	I/O			
			I2SC1_DO0	D	O			
			SDMMC1_DAT0	E	I/O			
			D13	F	I/O			
150	PA21	R13	PA21	Primary	I/O	PIO, I, PU, ST	3.3V	
			IRQ	A	I			
			PCK2	B	O			
			TCLK0	D	I			
			SDMMC1_DAT3	E	I/O			
			NANDRDY	F	I			
151	PA20	P12	PA20	Primary	I/O	PIO, I, PU, ST	3.3V	
			SPIO_NPCS3	A	O			
			TIOB0	B	I/O			
			SDMMC1_DAT2	E	I/O			
			D15	F	I/O			
152	PA23	U16	PA23	Primary	I/O	PIO, I, PU, ST	3.3V	
			FLEXCOM1_IO1	A	I/O			
			D1	B	I/O			
			TDI	C	I			
			SPI1_MOSI	D	I/O			

Num	Pin Name	BGA289 Ball	Pin Multiplexer			Reset state	Power Rail	Comment
			Signal Name	Mode	Type			
			QSPIO_CS	F	O			
153	PA22	U15	PA22	Primary	I/O	PIO, I, PU, ST	3.3V	
			FLEXCOM1_IO2	A	I/O			
			D0	B	I			
			TCK	C	I/O			
			SPI1_SPCK	D	I/O			
			SDMMC1_CK	E	I/O			
			QSPIO_SCK	F	O			
154	GND	-			Ground	Ground		
155	PA24	T15	PA24	Primary	I/O	PIO, I, PU, ST	3.3V	
			FLEXCOM1_IO0	A	I/O			
			D2	B	I/O			
			TDO	C	O			
			SPI1_MISO	D	I/O			
			QSPIO_IO0	F	I/O			
156	PA25	U17	PA25	Primary	I/O	PIO, I, PU, ST	3.3V	
			FLEXCOM1_IO3	A	O			
			D3	B	I/O			
			TMS	C	I			
			SPI1_NPCS0	D	I/O			
			QSPIO_IO1	F	I/O			
157	GND	-			Ground	Ground		
158	PA26	P13	PA26	Primary	I/O	PIO, I, PU, ST	3.3V	
			FLEXCOM1_IO4	A	O			
			D4	B	I/O			
			NTRST	C	I			
			SPI1_NPCS1	D	O			
			QSPIO_IO2	F	I/O			
159	USBA_DP	T8			A			
160	PA27	T16	PA27	Primary	I/O	PIO, I, PU, ST	3.3V	
			TIOA1	A	I/O			
			D5	B	I/O			
			SPIO_NPCS2	C	O			
			SPI1_NPCS2	D	O			
			SDMMC1_RSTN	E	O			
			QSPIO_IO3	F	I/O			
161	USBA_DM	R8			A			
162	PA28	R16	PA28	Primary	I/O	PIO, I, PU, ST	3.3V	
			TIOB1	A	I/O			
			D6	B	I/O			
			SPIO_NPCS3	C	O			
			SPI1_NPCS3	D	O			
			SDMMC1_CMD	E	I/O			
			CLASSD_L0	F	O			
163	GND	-			Ground	Ground		
164	PA29	T17	PA29	Primary	I/O	PIO, I, PU, ST	3.3V	
			TCLK1	A	I			
			D7	B	I/O			
			SPIO_NPCS1	C	O			
			SDMMC1_WP	E	I			
			CLASSD_L1	F	O			
165	USBB_DP	U8			A			
			PA30	Primary	I/O			

Num	Pin Name	BGA289 Ball	Pin Multiplexer			Reset state	Power Rail	Comment
			Signal Name	Mode	Type			
166	PA30	R15	NWE/NANDWE	B	O	PIO, I, PU, ST	3.3V	
			SPIO_NPCS0	C	I/O			
			PWMH0	D	O			
			SDMMC1_CD	E	I			
			CLASSD_L2	F	O			
167	USBB_DM	U9			A			
168	PA31	R17	PA31	Primary	I/O	PIO, I, PU, ST	3.3V	
			NCS3	B	O			
			SPIO_MISO	C	I/O			
			PWML0	D	O			
			CLASSD_L3	F	O			
169	GND	-	-	-	Ground		Ground	
170	GND	-	-	-	Ground		Ground	
171	HSIC_DATA	T9	-	-	-			
172	NC	-	-	-	-			
173	HSIC_STRB	U10	-	-	-			
174	NC	-	-	-	-			
175	GND	-	-	-	Ground		Ground	
176	GND	-	-	-	Ground		Ground	
177	NC	-	-	-	-			
178	JTAGSEL	T2	-	-	I	PU	VDDBU	
179	NC	-	-	-	-			
180	WKUP	P4	-	-	I	PU	VDDBU	
181	NC	-	-	-	-			
182	SHDN	R1	-	-	O	L	VDDBU	
183	NC	-	-	-	-			
184	NC	-	-	-	-			
185	GND	-	-	-	Ground			
186	NRST	U2	-	-	I	PU	VDDBU	
187	ETH0_RX-	-	-	-	A			
188	ACP	T1	-	-	-		VDDBU	
189	ETH0_RX+	-	-	-	A			
190	ACN	U1	-	-	-		VDDBU	
191	ETH0_TX-	-	-	-	A			
192	RXD	N4	-	-	O		VDDBU	
193	ETH0_TX+	-	-	-	A			
194	CLK_AUDIO	U3	-	-	O			
195	GND	-	-	-	Ground		Ground	
196	NC	-	-	-	-			
197	ETH0_LED2	-	-	-	O		3.3V	
198	NC	-	-	-	-			
199	ETH0_LED1	-	-	-	O		3.3V	
200	GND	-	-	-	Ground		Ground	