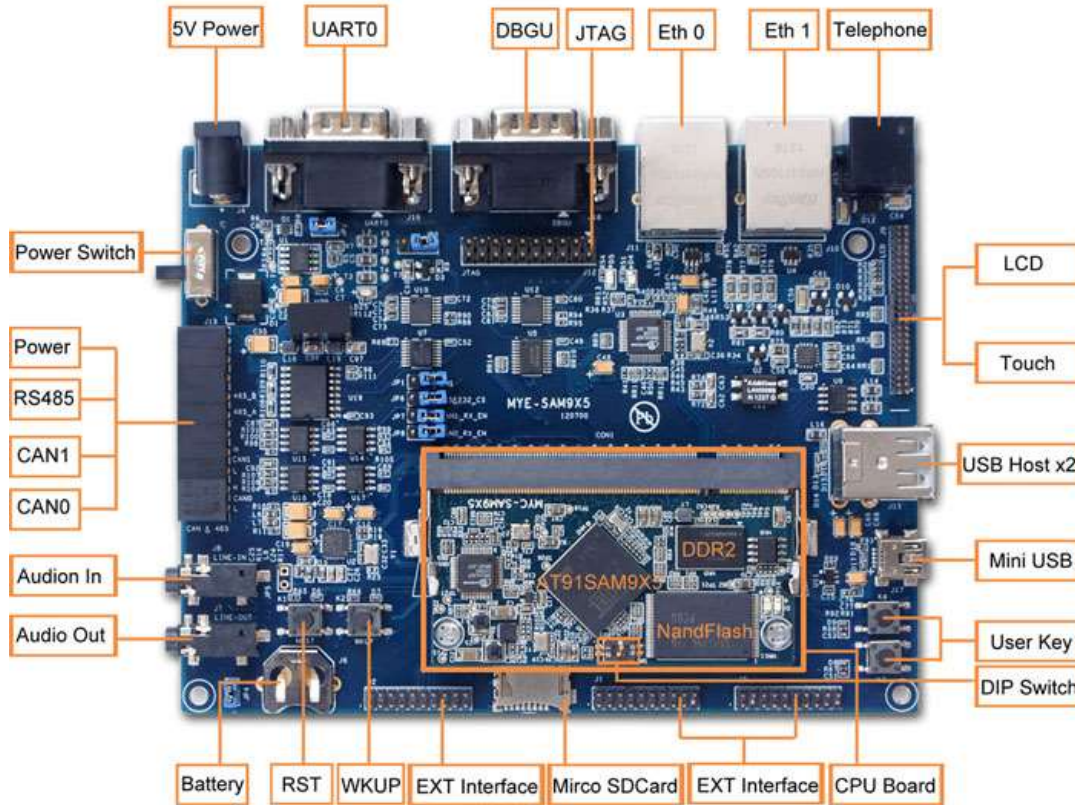


Signals Routed to SO-DIMM 200-pin Connector

MYIR has designed a base board for the MYC-SAM9X5 CPU Modules. The CPU Module is equipped with one SO-DIMM 200-pin connector which provides an interface to bring out all the I/O signals to or from the CPU Module and extended through the base board. Please refer to below table to know signals routed to the pins:



MYD-SAM9X5 Development Board (using MYC-SAM9X5 as controller board)



Function	IO Pin	Pin No.	Pin No.	IO Pin	Function
3V3 Power	+3V3	1	2	+3V3	3V3 Power
3V3 Power	+3V3	3	4	+3V3	3V3 Power
Power GND	GND	5	6	VBAT	RTC Battery
USB C_DP	USB C_DP	7	8	JTAGSEL	JTAGSEL
USB C_DM	USB C_DM	9	10	WKUP	WAKE UP
Power GND	GND	11	12	SHDN	SHDN
USB B_DM	USB B_DM	13	14	BMS	BMS
USB B_DP	USB B_DP	15	16	NRST	NRST

Power GND	GND	17	18	NTRST	NTRST
DIBP	DIBP	19	20	TDI	TDI
DIBN	DIBN	21	22	TCK	TCK
Power GND	GND	23	24	TMS	TMS
USB A_DM	USB A_DM	25	26	TDO	TDO
USB A_DP	USB A_DP	27	28	RTCK	RTCK
Power GND	GND	29	30	PWR_EN	PWR_EN
Blank	RFU1	31	32	RFU2	Blank
Blank	RFU3	33	34	RFU4	Blank
Blank	RFU5	35	36	RFU6	Blank
Blank	RFU7	37	38	RFU8	Blank
Blank	RFU9	39	40	RFU10	Blank
Power GND	GND	41	42	GND	Power GND
Blank	RFU11	43	44	RFU12	Blank
Blank	RFU13	45	46	RFU14	Blank
Blank	RFU15	47	48	RFU16	Blank
Blank	RFU17	49	50	RFU18	Blank
Power GND	GND	51	52	GND	Power GND
Blank	RFU19	53	54	RFU20	Blank
Blank	RFU21	55	56	RFU22	Blank
Blank	RFU23	57	58	RFU24	Blank
Blank	RFU25	59	60	RFU26	Blank
3V3 Power	VDDNF	61	62	VDDNF	3V3 Power
NAND	PD0	63	64	PD1	NAND
NAND	PD2	65	66	PD3	NAND
NAND	PD4	67	68	PD5	NAND
NAND	PD6	69	70	PD7	NAND
NAND	PD8	71	72	PD9	NAND
Blank	NC	73	74	GND	Power GND
NAND	PD10	75	76	PD11	NAND
NAND	PD12	77	78	PD13	NAND
GPIO	PD14	79	80	PD15	MCI0_CD
GPIO	PD16	81	82	PD17	GPIO
GPIO	PD18	83	84	PD19	GPIO
GPIO	PD20	85	86	PD21	GPIO
3V3 Power	VDDIOP0	87	88	VDDIOP0	3V3 Power
TXD0	PA0	89	90	PA1	RXD0
RTS0	PA2	91	92	PA3	CTS0
GPIO	PA4	93	94	GND	Power GND
SPIO_MISO	PA11	95	96	PA12	SPIO_MOSI
SPIO_SPCK	PA13	97	98	PA14	SPIO_NPCS0
Power GND	GND	99	100	PA7	TXD2
RXD2	PA8	101	102	PA21	SPI1_MISO
SPI1_MOSI	PA22	103	104	PA23	SPI1_SPCK
TWCK0	PA31	105	106	PA30	TWD0
Power GND	GND	107	108	PA15	MCI0_DA0

MCIO_CDA	PA16	109	110	PA17	MCIO_CK
MCIO_DA1	PA18	111	112	PA19	MCIO_DA2
MCIO_DA3	PA20	113	114	GND	Power GND
CANTX1	PA5	115	116	PA6	CANRX1
DTXD	PA10	117	118	PA9	DRXD
Power GND	GND	119	120	PA24	TK
TF	PA25	121	122	PA26	TD
RD	PA27	123	124	PA28	RK
RF	PA29	125	126	GND	Power GND
3V3 Power	VDDIOP1	127	128	VDDIOP1	3V3 Power
LCDDAT0	PC0	129	130	PC1	LCDDAT1
LCDDAT2	PC2	131	132	PC3	LCDDAT3
LCDDAT4	PC4	133	134	PC5	LCDDAT5
Power GND	GND	135	136	PC6	LCDDAT6
LCDDAT7	PC7	137	138	PC8	LCDDAT8
LCDDAT9	PC9	139	140	PC10	LCDDAT10
LCDDAT11	PC11	141	142	GND	Power GND
LCDDAT12	PC12	143	144	PC13	LCDDAT13
LCDDAT14	PC14	145	146	PC15	LCDDAT15
Power GND	GND	147	148	PC16	LCDDAT16
LCDDAT17	PC17	149	150	PC18	LCDDAT18
LCDDAT19	PC19	151	152	PC20	LCDDAT20
LCDDAT21	PC21	153	154	GND	Power GND
LCDDAT22	PC22	155	156	PC23	LCDDAT23
LCDDISP	PC24	157	158	PC25	GPIO
LCDPWM	PC26	159	160	PC27	LCDVSYNC
Power GND	GND	161	162	PC28	LCDHSYNC
LCDDEN	PC29	163	164	PC30	LCDPCK
GPIO	PC31	165	166	SELCONFIG	configuration signal
3V3 Power	VDDANA	167	168	VDDANA	3V3 Power
For Ethernet	PB0	169	170	PB1	For Ethernet
For Ethernet	PB2	171	172	PB3	For Ethernet
For Ethernet	PB4	173	174	PB5	For Ethernet
For Ethernet	PB6	175	176	PB7	For Ethernet
For Ethernet	PB8	177	178	GND	Power GND
For Ethernet	PB9	179	180	PB10	For Ethernet
AD0	PB11	181	182	PB12	AD1
AD2	PB13	183	184	PB14	AD3
GPIO	PB15	185	186	PB16	GPIO
GPIO	PB17	187	188	GND	Power GND
INT	PB18	189	190	ADVREF	ADC voltage reference
Power GND	GND	191	192	ETH0_LED0	ETH0_LED0
ETH0_TX+	ETH0_TX+	193	194	ETH0_LED1	ETH0_LED1
ETH0_TX+	ETH0_TX-	195	196	ETH0_LED2	ETH0_LED2
ETH0_RX+	ETH0_RX+	197	198	ETH0_AVDDT	ETH0_AVDDT
ETH0_RX+	ETH0_RX-	199	200	ETH0_GND	ETH0_GND