

Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
			Signal Name	Mode		
E10-E19	VSYS				5V	5V input
E20-E29	GND				0V	
E30-E39	VSYS				5V	5V input
E40-E49	GND				0V	
1	NVCC_SNVS_3V3				3.3V	Built in PMU chip(BD71837MWV) pin
2	VDD_3V3				3.3V	Built in PMU chip(BD71837MWV) pin
3	HDMI_DDC_SCL	R3	HDMI_DDC_SCL	Default		
4	ONOFF	W21	ONOFF	Default		
			ONOFF	ALT0		
5	HDMI_DDC_SDA	P3	HDMI_DDC_SDA	Default		
6	SYS_RESET					
7	HDMI_HPD	W2	HDMI_HPD	Default		
8	POR_B					
9	HDMI_CEC	W3	HDMI_CEC	Default		
10	BOOT_MODE0	W6	src.BOOT_MODE[0]	Default	3.3V	10K pull up to 3.3V on SOM
			src.BOOT_MODE[0]	ALT0		
11	GND					
12	BOOT_MODE1	V6	src.BOOT_MODE[1]	Default	3.3V	10K pull up to 3.3V on SOM
			src.BOOT_MODE[1]	ALT0		
13	HDMI_REFCLKN	R1	HDMI_REFCLKN	Default		
14	GND					
15	HDMI_REFCLKP	R2	HDMI_REFCLKP	Default		
16	JTAG_TCK	T5	sjc.TCK	Default	3.3V	10K pull down to gnd on SOM
			sjc.TCK	ALT0		
17	GND					
18	JTAG_TMS	V5	sjc.TMS	Default	3.3V	
			sjc.TMS	ALT0		
19	HDMI_TXN0	T2				
20	JTAG_TDI	W5	sjc.TDI	Default	3.3V	
			sjc.TDI	ALT0		
21	HDMI_TXP0	T1	gpt2.COMPARE2	ALT1	3.3V	10K pull up to 3.3V on SOM
22	JTAG_TDO	U5	sjc.TDO	Default	3.3V	
			sjc.TDO	ALT0		
23	GND					
24	JTAG_NTRST	U6	sjc.TRSTB	Default	3.3V	
			sjc.TRSTB	ALT0		
25	HDMI_TXN1	U1	HDMI_TXN1	Default		
26	GND					
27	HDMI_TXP1	U2	HDMI_TXP1	Default		
28	SAI1_RXFS	L1	GPIO4_IO00	Default	3.3V	
			SAI1.RX_SYNC	ALT0		
			SAI5.RX_SYNC	ALT1		
			CORESIGHT.TRACE_CLK	ALT4		
			GPIO4_IO00	ALT5		
30	SAI1_RXC	K1	GPIO4_IO01	Default	3.3V	
			SAI1.RX_BCLK	ALT0		
			SAI5.RX_BCLK	ALT1		
			CORESIGHT.TRACE_CTL	ALT4		
			GPIO4_IO01	ALT5		
31	HDMI_TXN2	N1	HDMI_TXN2	Default		
32	GND					
33	HDMI_TXP2	N2	HDMI_TXP2	Default		
34	SAI1_RXD0	K2	GPIO4_IO02	Default	3.3V	
			SAI1_RX_DATA0	ALT0		
			SAI5_RX_DATA0	ALT1		
			CORESIGHT.TRACE0	ALT4		
			GPIO4_IO02	ALT5		
			BOOT_CFG0	ALT6		
35	GND					
36	SAI1_RXD1	L2	GPIO4_IO03	Default	3.3V	
			SAI1_RX_DATA1	ALT0		
			SAI5_RX_DATA1	ALT1		
			CORESIGHT.TRACE1	ALT4		
			GPIO4_IO03	ALT5		
			BOOT_CFG1	ALT6		
37	HDMI_CLKN	M2	HDMI_CLKN	Default		

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38	SAI1_RXD2	H2	GPIO4_IO04	Default	3.3V	
			SAI1_RX_DATA2	ALT0		
			SAI5_RX_DATA2	ALT1		
			CORESIGHT_TRACE2	ALT4		
			GPIO4_IO04	ALT5		
			BOOT_CFG2	ALT6		
39	HDMI_CLKP	M1	HDMI_CLKP	Default		
40	SAI1_RXD3	J2	GPIO4_IO05	Default	3.3V	
			SAI1_RX_DATA3	ALT0		
			SAI5_RX_DATA3	ALT1		
			CORESIGHT_TRACE3	ALT4		
			GPIO4_IO05	ALT5		
			BOOT_CFG3	ALT6		
41	GND					
42	SAI1_RXD4	J1	GPIO4_IO06	Default	3.3V	
			SAI1_RX_DATA4	ALT0		
			SAI6_TX_BCLK	ALT1		
			SAI6_RX_BCLK	ALT2		
			CORESIGHT_TRACE4	ALT4		
			GPIO4_IO06	ALT5		
BOOT_CFG4	ALT6					
43	HDMI_AUXN	V2	HDMI_AUXN	Default		
44	SAI1_RXD5	F1	GPIO4_IO07	Default	3.3V	
			SAI1_RX_DATA5	ALT0		
			SAI6_TX_DATA0	ALT1		
			SAI6_RX_DATA0	ALT2		
			SAI1_RX_SYNC	ALT3		
			CORESIGHT_TRACES5	ALT4		
			GPIO4_IO07	ALT5		
			BOOT_CFG5	ALT6		
45	HDMI_AUXP	V1	HDMI_AUXP	Default		
46	SAI1_RXD6	G2	GPIO4_IO08	Default	3.3V	
			SAI1_RX_DATA6	ALT0		
			SAI6_TX_SYNC	ALT1		
			SAI6_RX_SYNC	ALT2		
			CORESIGHT_TRACE6	ALT4		
			GPIO4_IO08	ALT5		
BOOT_CFG6	ALT6					
47	GND					
48	SAI1_RXD7	G1	GPIO4_IO09	Default	3.3V	
			SAI1_RX_DATA7	ALT0		
			SAI6_MCLK	ALT1		
			SAI1_TX_SYNC	ALT2		
			SAI1_TX_DATA4	ALT3		
			CORESIGHT_TRACE7	ALT4		
			GPIO4_IO09	ALT5		
			BOOT_CFG7	ALT6		
49	SAI5_MCLK	K4	GPIO3_IO25	Default	3.3V	
			SAI5_MCLK	ALT0		
			SAI1_TX_BCLK	ALT1		
			SAI4_MCLK	ALT2		
			GPIO3_IO25	ALT5		
50	GND					
51	SAI5_RXFS	N4	GPIO3_IO19	Default	3.3V	
			SAI5_RX_SYNC	ALT0		
			SAI1_TX_DATA0	ALT1		
			GPIO3_IO19	ALT5		
52	SAI1_MCLK	A3	GPIO4_IO20	Default	3.3V	
			SAI5_MCLK	ALT0		
			SAI5_MCLK	ALT1		
			SAI1_TX_BCLK	ALT2		
			GPIO4_IO20	ALT5		
53	SAI5_RXC	L5	GPIO3_IO20	Default	3.3V	
			SAI5_RX_BCLK	ALT0		
			SAI1_TX_DATA1	ALT1		
			GPIO3_IO20	ALT5		
			GPIO4_IO10	Default		

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54	SAI1_TXFS	H1	SAI1_TX_SYNC	ALT0	3.3V	
			SAI5_TX_SYNC	ALT1		
			CORESIGHT_EVENT0	ALT4		
			GPIO4_IO10	ALT5		
55	SAI5_RXD0	M5	GPIO3_IO21	Default	3.3V	
			SAI5_RX_DATA0	ALT0		
			SAI1_TX_DATA2	ALT1		
			GPIO3_IO21	ALT5		
56	SAI1_TXC	E1	GPIO4_IO11	Default	3.3V	
			SAI1_TX_BCLK	ALT0		
			SAI5_TX_BCLK	ALT1		
			CORESIGHT_EVENT1	ALT4		
57	SAI5_RXD1	L4	GPIO4_IO11	ALT5	3.3V	
			GPIO3_IO22	Default		
			SAI5_RX_DATA1	ALT0		
			SAI1_TX_DATA3	ALT1		
			SAI1_TX_SYNC	ALT2		
58	GND		SAI5_TX_SYNC	ALT3		
			GPIO3_IO22	ALT5		
59	SAI5_RXD2	M4	GPIO3_IO23	Default	3.3V	
			SAI5_RX_DATA2	ALT0		
			SAI1_TX_DATA4	ALT1		
			SAI1_TX_SYNC	ALT2		
			SAI5_TX_BCLK	ALT3		
60	SAI1_TXD0	F2	GPIO3_IO23	ALT5	3.3V	
			GPIO4_IO12	Default		
			SAI1_TX_DATA0	ALT0		
			SAI5_TX_DATA0	ALT1		
			CORESIGHT_TRACE8	ALT4		
			GPIO4_IO12	ALT5		
61	SAI5_RXD3	K5	BOOT_CFG8	ALT6	3.3V	
			GPIO3_IO24	Default		
			SAI5_RX_DATA3	ALT0		
			SAI1_TX_DATA5	ALT1		
			SAI1_TX_SYNC	ALT2		
			SAI5_TX_DATA0	ALT3		
62	SAI1_TXD1	E2	GPIO3_IO24	ALT5	3.3V	
			GPIO4_IO13	Default		
			SAI1_TX_DATA1	ALT0		
			SAI5_TX_DATA1	ALT1		
			CORESIGHT_TRACE9	ALT4		
			GPIO4_IO13	ALT5		
63	GND		BOOT_CFG9	ALT6		
64	SAI1_TXD2	B2	GPIO4_IO14	Default	3.3V	
			SAI1_TX_DATA2	ALT0		
			SAI5_TX_DATA2	ALT1		
			CORESIGHT_TRACE9010	ALT4		
			GPIO4_IO14	ALT5		
			BOOT_CFG10	ALT6		
65	SAI2_MCLK	H5	GPIO4_IO14	ALT6	3.3V	
			GPIO4_IO27	Default		
			SAI2_MCLK	ALT0		
			SAI5_MCLK	ALT1		
66	SAI1_TXD3	D1	GPIO4_IO27	ALT5	3.3V	
			GPIO4_IO15	Default		
			SAI1_TX_DATA3	ALT0		
			SAI5_TX_DATA3	ALT1		
			CORESIGHT_TRACE9011	ALT4		
			GPIO4_IO15	ALT5		
67	SAI2_TXFS	H4	BOOT_CFG11	ALT6	3.3V	
			GPIO4_IO24	Default		
			SAI2_TX_SYNC	ALT0		
			SAI5_TX_DATA1	ALT1		
			GPIO4_IO24	ALT5		
			GPIO4_IO016	Default		
			SAI1_TX_DATA4	ALT0		

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			Signal Name	Mode		
68	SAI1_TXD4	D2	SAI6_RX_BCLK	ALT1	3.3V	
			SAI6_TX_BCLK	ALT2		
			CORESIGHT_TRACE12	ALT4		
			GPIO4_IO016	ALT5		
			BOOT_CFG12	ALT6		
69	SAI2_TXC	J5	GPIO4_IO25	Default	3.3V	
			SAI2_TX_SYNC	ALT0		
			SAI5_TX_DATA1	ALT1		
			GPIO4_IO25	ALT5		
70	SAI1_TXD5	C2	GPIO4_IO017	Default	3.3V	
			SAI1_TX_DATA5	ALT0		
			SAI6_RX_DATA0	ALT1		
			SAI6_TX_DATA0	ALT2		
			CORESIGHT_TRACE13	ALT4		
			GPIO4_IO017	ALT5		
			BOOT_CFG13	ALT6		
71	SAI2_TXD	G5	GPIO4_IO26	Default	3.3V	
			SAI2_TX_DATA0	ALT0		
			SAI5_TX_DATA3	ALT1		
			GPIO4_IO26	ALT5		
72	SAI1_TXD6	B3	GPIO4_IO018	Default	3.3V	
			SAI1_TX_DATA6	ALT0		
			SAI6_RX_SYNC	ALT1		
			SAI6_TX_SYNC	ALT2		
			CORESIGHT_TRACE14	ALT4		
			GPIO4_IO018	ALT5		
73	GND					
74	SAI1_TXD7	C1	GPIO4_IO19	Default	3.3V	
			SAI1_TX_DATA7	ALT0		
			SAI6_MCLK	ALT1		
			CORESIGHT_TRACE9015	ALT4		
			GPIO4_IO19	ALT5		
			BOOT_CFG15	ALT6		
75	SAI2_RXFS	L1	GPIO4_IO21	Default	3.3V	
			SAI2_RX_SYNC	ALT0		
			SAI5_TX_SYNC	ALT1		
			GPIO4_IO21	ALT5		
76	GND					
77	SAI2_RXC	H3	GPIO4_IO22	Default	3.3V	
			SAI2_RX_BCLK	ALT0		
			SAI5_TX_BCLK	ALT1		
			GPIO4_IO22	ALT5		
78	SAI3_MCLK	D3	GPIO5_IO02	Default	3.3V	
			SAI3_MCLK	ALT0		
			PWM4_OUT	ALT1		
			SAI5_MCLK	ALT2		
			GPIO5_IO02	ALT5		
79	SAI2_RXD	H6	GPIO4_IO23	Default	3.3V	
			SAI2_RX_DATA0	ALT0		
			SAI5_TX_DATA0	ALT1		
			GPIO4_IO23	ALT5		
80	SAI3_TXFS	G3	GPIO4_IO31	Default	3.3V	
			SAI3_TX_SYNC	ALT0		
			GPT1_CLK	ALT1		
			SAI5_RX_DATA1	ALT2		
			GPIO4_IO31	ALT5		
81	GND					
82	SAI3_TXC	C4	GPIO5_IO00	Default	3.3V	
			SAI3_TX_BCLK	ALT0		
			GPT1_COMPARE2	ALT1		
			SAI5_RX_DATA2	ALT2		
			GPIO5_IO00	ALT5		
83	SPDIF_TX	F6	GPIO5_IO03	Default	3.3V	
			SPDIF1_OUT	ALT0		
			PWM3_OUT	ALT1		
			GPIO5_IO03	ALT5		
			GPIO5_IO01	Default		

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			Signal Name	Mode		
84	SAI3_TXD	C3	SAI3_TX_DATA0	ALT0	3.3V	
			GPT1_COMPARE3	ALT1		
			SAI5_RX_DATA3	ALT2		
			GPIO5_IO01	ALT5		
85	SPDIF_RX	G6	GPIO5_IO04	Default	3.3V	
			SPDIF1_IN	ALT0		
			PWM2_OUT	ALT1		
			GPIO5_IO04	ALT5		
86	SAI3_RXFS	N4	GPIO4_IO28	Default	3.3V	
			SAI3_RX_SYNC	ALT0		
			GPT1_CAPTURE1	ALT1		
			SAI5_RX_SYNC	ALT2		
87	SPDIF_EXT_CLK	E6	GPIO5_IO05	Default	3.3V	
			SPDIF1_EXT_CLK	ALT0		
			PWM1_OUT	ALT1		
			GPIO5_IO05	ALT5		
88	SAI3_RXC	F4	GPIO4_IO29	Default	3.3V	
			SAI3_RX_BCLK	ALT0		
			GPT1_CAPTURE2	ALT1		
			SAI5_RX_BCLK	ALT2		
89	GND		GPIO4_IO29	ALT5		
90	SAI3_RXD	F3	GPIO4_IO30	Default	3.3V	
			SAI3_RX_DATA0	ALT0		
			GPT1_COMPARE1	ALT1		
			SAI5_RX_DATA0	ALT2		
91	UART1_RXD	C7	GPIO4_IO30	ALT5	3.3V	Debug UART
			GPIO5_IO022	Default		
			UART1_RX	ALT0		
			ECSPi3_SCLK	ALT1		
92	GND		GPIO5_IO022	ALT5		
93	UART1_TXD	A7	GPIO5_IO023	Default	3.3V	Debug UART
			UART1_TX	ALT0		
			ECSPi3_MOSI	ALT1		
			GPIO5_IO023	ALT5		
94	GPIO1_IO00	T6	GPIO1_IO00	Default	3.3V	
			GPIO1_IO00	ALT0		
			CCM_ENET_PHY_REF_CLK_ROOT	ALT1		
			ANAMIX_REF_CLK_32K	ALT5		
95	UART2_RXD	B6	CCM_EXT_CLK1	ALT6	3.3V	
			GPIO5_IO024	Default		
			UART2_RX	ALT0		
			ECSPi3_MISO	ALT1		
96	GPIO1_IO01	T7	GPIO5_IO024	ALT5	3.3V	
			GPIO1_IO01	Default		
			GPIO1_IO01	ALT0		
			PWM1_OUT	ALT1		
97	UART2_TXD	D6	ANAMIX_REF_CLK_24M	ALT5	3.3V	
			CCM_EXT_CLK2	ALT6		
			GPIO5_IO025	Default		
			UART2_TX	ALT0		
98	GPIO1_IO03	P4	ECSPi3_SSO	ALT1	3.3V	
			GPIO5_IO025	ALT5		
			GPIO1_IO03	Default		
			GPIO1_IO03	ALT0		
99	UART3_RXD	A6	USDHC1_VSELECT	ALT1	3.3V	
			SDMA1_EXT_EVENT0	ALT5		
			GPIO5_IO026	Default		
			UART3_RX	ALT0		
100	GPIO1_IO05	P7	UART1_CTS_B	ALT1	3.3V	
			GPIO5_IO026	ALT5		
			GPIO1_IO05	Default		
			GPIO1_IO05	ALT0		
			M4_NMI	ALT1	3.3V	
			CCM_PMIC_READY	ALT5		

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			Signal Name	Mode		
101	UART3_TXD	B7	GPIO5_IO027	Default	3.3V	
			UART3_TX	ALT0		
			UART1_RTS_B	ALT1		
			GPIO5_IO027	ALT5		
102	GPIO1_IO06	N5	GPIO1_IO06	Default	3.3V	
			GPIO1_IO06	ALT0		
			ENET1_MDC	ALT1		
			USDHC1_CD_B	ALT5		
103	UART4_RXD	C6	CCM_EXT_CLK3	ALT6	3.3V	
			GPIO5_IO028	Default		
			UART4_RX	ALT0		
			UART2_CTS_B	ALT1		
104	GPIO1_IO10	M7	PCIE1_CLKREQ_B	ALT2	3.3V	
			GPIO5_IO028	ALT5		
			GPIO1_IO010	Default		
			GPIO1_IO10	ALT0		
105	UART4_TXD	D7	USB1_OTG_ID	ALT1	3.3V	
			GPIO5_IO029	Default		
			UART4_TX	ALT0		
			UART2_RTS_B	ALT1		
106	GPIO1_IO08	N7	PCIE2_CLKREQ_B	ALT2	3.3V	
			GPIO5_IO029	ALT5		
			GPIO1_IO08	Default		
			GPIO1_IO08	ALT0		
107	GND		ENET1_1588_EVENT0_IN	ALT1	3.3V	
			USDHC2_RESET_B	ALT5		
			GPIO1_IO12	Default		
			GPIO1_IO12	ALT0		
108	GPIO1_IO12	L7	USB1_OTG_PWR	ALT1	3.3V	
			SDMA2_EXT_EVENT1	ALT5		
			GPIO1_IO12	Default		
			GPIO1_IO12	ALT0		
109	I2C1_SCL	E7	GPIO5_IO014	Default	3.3V	
			I2C1_SCL	ALT0		
			ENET1_MDC	ALT1		
			GPIO5_IO014	ALT5		
110	GPIO1_IO13	K6	GPIO1_IO13	Default	3.3V	
			GPIO1_IO13	ALT0		
			USB1_OTG_OC	ALT1		
			PWM2_OUT	ALT5		
111	I2C1_SDA	E8	GPIO5_IO015	Default	3.3V	
			I2C1_SDA	ALT0		
			ENET1_MDIO	ALT1		
			GPIO5_IO015	ALT5		
112	GPIO1_IO14	K7	GPIO1_IO14	Default	3.3V	
			GPIO1_IO14	ALT0		
			USB2_OTG_PWR	ALT1		
			PWM3_OUT	ALT5		
113	I2C2_SCL	G7	CCM_CLKO1	ALT6	3.3V	
			GPIO5_IO016	Default		
			I2C2_SCL	ALT0		
			ENET1_1588_EVENT1_IN	ALT1		
114	GPIO1_IO15	J6	GPIO5_IO016	ALT5	3.3V	
			GPIO1_IO15	Default		
			GPIO1_IO15	ALT0		
			USB2_OTG_OC	ALT1		
115	I2C2_SDA	F7	PWM4_OUT	ALT5	3.3V	
			CCM_CLKO2	ALT6		
			GPIO1_IO15	Default		
			GPIO1_IO15	ALT0		
116	GND		ENET1_1588_EVENT1_OUT	ALT1	3.3V	
			GPIO5_IO017	Default		
			I2C2_SDA	ALT0		
			GPIO5_IO017	ALT5		
117	I2C3_SCL	G8	GPIO5_IO018	Default	3.3V	
			I2C3_SCL	ALT0		
			PWM4_OUT	ALT1		
			GPT2_CLK	ALT2		
			GPIO5_IO018	ALT5		
			GPIO5_IO09	Default		

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118	ECSPI1_SSO	D4	ECSPI1_SSO	ALT0	3.3V	
			UART3_RTS_B	ALT1		
			GPIO5_IO09	ALT5		
119	I2C3_SDA	E9	GPIO5_IO019	Default	3.3V	
			I2C3_SDA	ALT0		
			PWM3_OUT	ALT1		
			GPT3_CLK	ALT2		
			GPIO5_IO019	ALT5		
120	ECSPI1_MOSI	A4	GPIO5_IO07	Default	3.3V	
			ECSPI1_MOSI	ALT0		
			UART3_TX	ALT1		
			GPIO5_IO07	ALT5		
121	I2C4_SCL	F8	GPIO5_IO020	Default	3.3V	
			I2C4_SCL	ALT0		
			PWM2_OUT	ALT1		
			PCIE1_CLKREQ_B	ALT2		
			GPIO5_IO020	ALT5		
122	ECSPI1_MISO	B4	GPIO5_IO08	Default	3.3V	
			ECSPI1_MISO	ALT0		
			UART3_CTS_B	ALT1		
			GPIO5_IO08	ALT5		
123	I2C4_SDA	F9	GPIO5_IO021	Default	3.3V	
			I2C4_SDA	ALT0		
			PWM1_OUT	ALT1		
			PCIE2_CLKREQ_B	ALT2		
			GPIO5_IO021	ALT5		
124	ECSPI1_SCLK	D5	GPIO5_IO06	Default	3.3V	
			ECSPI1_SCLK	ALT0		
			UART3_RX	ALT1		
			GPIO5_IO06	ALT5		
125	GND					
133	GND					
134	GND					
135	ECSPI2_SSO	A5	GPIO5_IO013	Default	3.3V	
			ECSPI2_SSO	ALT0		
			UART4_RTS_B	ALT1		
			GPIO5_IO013	ALT5		
136	USB2_RXN	B8	USB2_RXN	Default		
137	ECSPI2_MOSI	E5	GPIO5_IO011	Default	3.3V	
			ECSPI2_MOSI	ALT0		
			UART4_TX	ALT1		
			GPIO5_IO011	ALT5		
138	USB2_RXP	A8	USB2_RXN	Default		
139	ECSPI2_MISO	B5	GPIO5_IO012	Default	3.3V	
			ECSPI2_MISO	ALT0		
			UART4_CTS_B	ALT1		
			GPIO5_IO012	ALT5		
140	GND					
141	ECSPI2_SCLK	C5	GPIO5_IO010	Default	3.3V	
			ECSPI2_SCLK	ALT0		
			UART4_RX	ALT1		
			GPIO5_IO010	ALT5		
142	USB2_TXN	B9	USB2_TXN	Default		
143	GND					
144	USB2_TXP	A9	USB2_TXP	Default		
145	DSI_CKN	C16	DSI_CKN	Default		
146	GND					
147	DSI_CKP	D16	DSI_CKP	Default		
148	USB2_DP	A10	USB2_DP	Default		
149	GND					
150	USB2_DN	B10	USB2_DN	Default		
151	DSI_DN0	A17	DSI_DN0	Default		
152	GND					
153	DSI_DPO	B17	DSI_DPO	Default		
154	USB2_ID	C9	USB2_ID	Default		
155	GND					
156	USB2_VBUS	D9	USB2_VBUS	Default		
157	DSI_DN1	A16	DSI_DN1	Default		

Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
			Signal Name	Mode		
158	USB1_ID	C14	USB1_ID	Default		
159	DSI_DP1	B16	DSI_DP1	Default		
160	USB1_VBUS	D14	USB1_VBUS	Default		
161	GND					
162	GND					
163	DSI_DN2	A18	DSI_DN2	Default		
164	USB1_RXN	B12	USB1_RXN	Default		
165	DSI_DP2	B18	DSI_DP2	Default		
166	USB1_RXP	A12	USB1_RXP	Default		
167	GND					
168	GND					
169	DSI_DN3	A15	DSI_DN3	Default		
170	USB1_TXN	B13	USB1_TXN	Default		
171	DSI_DP3	B15	DSI_DP3	Default		
172	USB1_TXP	A13	USB1_TXP	Default		
173	GND					
174	GND					
175	CSI_P2_CKN	A19	CSI_P2_CKN	Default		
176	USB1_DP	A14	USB1_DP	Default		
177	CSI_P2_CKP	B19	CSI_P2_CKP	Default		
178	USB1_DN	B14	USB1_DN	Default		
179	GND					
180	GND					
181	CSI_P2_DN0	C20	CSI_P2_DN0	Default		
182	CSI_P1_CKN	A22	CSI_P1_CKN	Default		
183	CSI_P2_DP0	D20	CSI_P2_DP0	Default		
184	CSI_P1_CKP	B22	CSI_P1_CKP	Default		
185	GND					
186	GND					
187	CSI_P2_DN1	A20	CSI_P2_DN1	Default		
188	CSI_P1_DN0	A23	CSI_P1_DN0	Default		
189	CSI_P2_DP1	B20	CSI_P2_DP1	Default		
190	CSI_P1_DP0	B23	CSI_P1_DP0	Default		
191	GND					
192	GND					
193	CSI_P2_DN2	A21	CSI_P2_DN2	Default		
194	CSI_P1_DN1	C22	CSI_P1_DN1	Default		
195	CSI_P2_DP2	B21	CSI_P2_DP2	Default		
196	CSI_P1_DP1	D22	CSI_P1_DP1	Default		
197	GND					
198	GND					
199	CSI_P2_DN3	C19	CSI_P2_DN3	Default		
200	CSI_P1_DN2	B24	CSI_P1_DN2	Default		
201	CSI_P2_DP3	D19	CSI_P2_DP3	Default		
202	CSI_P1_DP2	C23	CSI_P1_DP2	Default		
203	GND					
204	GND					
205	NAND_nRE	K19	GPIO3_IO15	Default	3.3V	
			RAWNAND_RE_B	ALT0		
			QSPI_B_DQS	ALT1		
			GPIO3_IO15	ALT5		
206	CSI_P1_DN3	C21	CSI_P1_DN3	Default		
207	NAND_nREADY	K20	GPIO3_IO16	Default	3.3V	
			RAWNAND_READY_B	ALT0		
			GPIO3_IO16	ALT5		
208	CSI_P1_DP3	D21	CSI_P1_DP3	Default		
209	NAND_ALE	G19	GPIO3_IO00	Default	3.3V	
			RAWNAND_ALE	ALT0		
			QSPI_A_SCLK	ALT1		
			GPIO3_IO00	ALT5		
211	NAND_CLE	H21	GPIO3_IO05	Default	3.3V	
			RAWNAND_CLE	ALT0		
			QSPI_B_SCLK	ALT1		
			GPIO3_IO05	ALT5		
212	CLK1_P	R23	CLK1_P	Default		
213	GND					
214	CLK1_N	T23	CLK1_N	Default		

Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
			Signal Name	Mode		
215	NAND_nWE	K22	GPIO3_IO17	Default	3.3V	
			RAWNAND_WE_B	ALT0		
			GPIO3_IO17	ALT5		
216	GND					
217	NAND_nWP	K21	GPIO3_IO18	Default	3.3V	
			RAWNAND_WP_B	ALT0		
			GPIO3_IO18	ALT5		
218	CLK2_P	T22	CLK2_P	Default		
219	NAND_DQS	M20	GPIO3_IO14	Default	3.3V	
			RAWNAND_DQS	ALT0		
			QSPI_A_DQS	ALT1		
220	CLK2_N	U22	GPIO3_IO14	ALT5		
			CLK2_N	Default		
221	NAND_nCE0	H19	GPIO3_IO01	Default	3.3V	
			RAWNAND_CE0_B	ALT0		
			QSPI_A_SS0_B	ALT1		
222	GND		GPIO3_IO01	ALT5		
223	NAND_nCE1	G21	GPIO3_IO02	Default	3.3V	
			RAWNAND_CE1_B	ALT0		
			QSPI_A_SS1_B	ALT1		
224	PCIE2_RXN	D24	GPIO3_IO02	ALT5		
			PCIE2_RXN	Default		
225	NAND_nCE2	F21	GPIO3_IO03	Default	3.3V	
			RAWNAND_CE2_B	ALT0		
			QSPI_B_SS0_B	ALT1		
226	PCIE2_RXP	D25	GPIO3_IO03	ALT5		
			PCIE2_RXP	Default		
227	NAND_nCE3	H20	GPIO3_IO04	Default	3.3V	
			RAWNAND_CE3_B	ALT0		
			QSPI_B_SS1_B	ALT1		
228	GND		GPIO3_IO04	ALT5		
229	GND					
230	PCIE2_TXN	E24	PCIE2_TXN	Default		
231	NAND_DATA7	M19	GPIO3_IO13	Default	3.3V	
			RAWNAND_DATA07	ALT0		
			QSPI_B_DATA3	ALT1		
232	PCIE2_TXP	E25	GPIO3_IO13	ALT5		
			PCIE2_TXP	Default		
233	NAND_DATA6	L19	GPIO3_IO12	Default	3.3V	
			RAWNAND_DATA06	ALT0		
			QSPI_B_DATA2	ALT1		
234	GND		GPIO3_IO12	ALT5		
235	NAND_DATA5	J22	GPIO3_IO11	Default	3.3V	
			RAWNAND_DATA05	ALT0		
			QSPI_B_DATA1	ALT1		
236	PCIE2_REF_CLKN	F24	GPIO3_IO11	ALT5		
			PCIE2_REF_CLKN	Default		
237	NAND_DATA4	L20	GPIO3_IO10	Default	3.3V	
			RAWNAND_DATA04	ALT0		
			QSPI_B_DATA0	ALT1		
238	PCIE2_REF_CLKP	F25	GPIO3_IO10	ALT5		
			PCIE2_REF_CLKN	Default		
239	GND					
240	GND					
241	NAND_DATA3	J21	GPIO3_IO09	Default	3.3V	
			RAWNAND_DATA03	ALT0		
			QSPI_A_DATA3	ALT1		
242	PCIE1_RXN	H24	GPIO3_IO09	ALT5		
			PCIE1_RXN	Default		
243	NAND_DATA2	H22	GPIO3_IO08	Default	3.3V	
			RAWNAND_DATA02	ALT0		
			QSPI_A_DATA2	ALT1		
244	PCIE1_RXP	H25	GPIO3_IO08	ALT5		
			PCIE1_RXP	Default		
			GPIO3_IO07	Default		

Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
			Signal Name	Mode		
245	NAND_DATA1	J20	RAWNAND_DATA01	ALT0	3.3V	
			QSPI_A_DATA1	ALT1		
			GPIO3_IO07	ALT5		
246	GND					
247	NAND_DATA0	G20	GPIO3_IO06	Default	3.3V	
			RAWNAND_DATA00	ALT0		
			QSPI_A_DATA0	ALT1		
			GPIO3_IO06	ALT5		
248	PCIE1_TXN	J24	PCIE1_TXN	Default		
249	GND					
250	PCIE1_TXP	J25	PCIE1_TXP	Default		
251	ENET_LED_100				3.3V	Built in Ethernet PHY chip(AR8035-AL) pin
252	GND					
253	ENET_LED_1000				3.3V	Built in Ethernet PHY chip(AR8035-AL) pin
254	PCIE1_REF_CLKN	K24	PCIE1_REF_CLKN	Default		
255	ENET_LED_ACT				3.3V	Built in Ethernet PHY chip(AR8035-AL) pin
256	PCIE1_REF_CLKP	K25	PCIE1_REF_CLKP	Default		
257	GND					
258	GND					
259	ETH_TRP3				A	Built in Ethernet PHY chip(AR8035-AL) pin
260	SD2_WP	M21	GPIO2_IO20	Default	3.3V	
			USDHC2_WP	ALT0		
			GPIO2_IO20	ALT5		
261	ETH_TRN3				A	Built in Ethernet PHY chip(AR8035-AL) pin
262	SD2_nCD	L21	GPIO2_IO12	Default	3.3V	
			USDHC2_CD_B	ALT0		
			GPIO2_IO12	ALT5		
263	GND					
264	SD2_CMD	M22	GPIO2_IO14	Default	3.3V	
			USDHC2_CMD	ALT0		
			GPIO2_IO14	ALT5		
265	ETH_TRP2				A	Built in Ethernet PHY chip(AR8035-AL) pin
266	SD2_CLK	L22	GPIO2_IO13	Default	3.3V	
			USDHC2_CLK	ALT0		
			GPIO2_IO13	ALT5		
267	ETH_TRN2				A	Built in Ethernet PHY chip(AR8035-AL) pin
268	GND					
269	GND					
270	SD2_DATA3	P21	GPIO2_IO18	Default	3.3V	
			USDHC2_DATA3	ALT0		
			GPIO2_IO18	ALT5		
271	ETH_TRP1				A	Built in Ethernet PHY chip(AR8035-AL) pin
272	SD2_DATA2	P22	GPIO2_IO17	Default	3.3V	
			USDHC2_DATA2	ALT0		
			GPIO2_IO17	ALT5		
273	ETH_TRN1				A	Built in Ethernet PHY chip(AR8035-AL) pin
274	SD2_DATA1	N21	GPIO2_IO16	Default	3.3V	
			USDHC2_DATA1	ALT0		
			GPIO2_IO16	ALT5		
275	GND					
276	SD2_DATA0	N22	GPIO2_IO15	Default	3.3V	
			USDHC2_DATA0	ALT0		
			GPIO2_IO15	ALT5		
277	ETH_TRP0				A	Built in Ethernet PHY chip(AR8035-AL) pin
278	SD2_nRST	R22	GPIO2_IO19	Default	3.3V	
USDHC2_RESET_B			ALT0			
GPIO2_IO19			ALT5			
279	ETH_TRN0				A	Built in Ethernet PHY chip(AR8035-AL) pin
280	VDD_SD2				3.3V	Built in PMU chip(BD71837MWV) pin
281	GND					